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hole [12] in a way similar to a gradient index acoustic lens, with outer shell deflecting the acoustic waves and directing them towards the center of a structure, where the vibrations are dissipated. This method has the advantage of a large bandwidth at low operating frequencies (around several kHz), 80% of incident acoustic energy absorption, while the main drawback is relatively large cm-scale size of a system, which makes it unusable for miniaturized systems.

The summary of the existing acoustic isolation techniques in MEMS is presented in the Table 1. It can be seen that various solutions can be used for different frequencies ranges, but not all of them can be miniaturized and, thus, applied to the pMUT array case.

| Acoustic       | Typical     | Can be       |
|----------------|-------------|--------------|
| isolation type | operating   | easily       |
|                | frequencies | miniaturized |
| Mesa structure | ~1-10 GHz   | Yes          |
| Bragg mirror   | ~1-10 GHz   | No           |
| Polymer-based  | 1 Hz - 100  | Yes          |
|                | kHz         |              |
| Metamaterial-  | 1 Hz – 10   | No           |
| based          | GHz         |              |

Table 1. Comparison of acoustic isolation strategies in MEMS

# 3. Methodology

A typical pMUT device-based array is studied in this paper, and the performance of various acoustic isolation techniques is explored. A schematic on an individual pMUT stack is shown on the Fig. 1. It is composed of two aluminum electrodes covering a piezoelectric AlN layer, and a silicon nitride layer deposited on the top of the stack. A thin void volume is etched on the back side of the stack.



Figure 1: Schematics of an individual MEMS pMUT device

An overall pMUT array schematic is demonstrated on the Fig. 2. It consists of 9 individual MEMS pMUTs, however, for a sake of simplicity a 2D section is made for a series of analyses. A 50 $\mu$ m layer of silicon is added on a backside to simulate an array assembly in context of packaging.



# Figure 2: Schematics of a MEMS pMUT array with a section used to build 2D model

A cross section consisting 3 pMUT devices used in Comsol model is depicted on the Fig. 3. The deformation is applied to the central device at resonant frequency simulating the excitation of an operating mode and the deformation on the surface of a neighboring pMUT is evaluated.



### Figure 3: Schematics of a model cross-section used to evaluate crosstalk between neighboring pMUT devices

Several configurations are studied to evaluate the vibration suppression effect with Comsol software in 2D configuration. They are demonstrated on the following figures: mesa-type structure (Fig. 4), ring pillar structure (made of silicon oxide or polymer) (Fig. 8) and a combination of mesa-type structure with deposited polymer layer (Fig. 11). To perform the analysis, a sine excitation force of the same frequency and amplitude is applied to the membrane area on the surface of the device for every configuration. Low reflecting boundary conditions (as defined in Comsol) are applied to the edges of a model to assure the absence of artificial acoustic reflections. A frequency domain study at the first resonant mode (15MHz) is performed at the typical deformation amplitude and the maximum deformation on the neighboring device surface is evaluated. Obtained values are compared to the case where no acoustic isolation solution is applied

# 4. Results

First, a crosstalk in the array without acoustic isolation is evaluated (configuration from Fig. 3). For a typical central pMUT excitation amplitude of 100nm, the excitation on the neighboring device is evaluated and found to be equal to 2nm. The amplitude of the excitation measured on the neighboring device while the central one

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is being excited is referred as a crosstalk amplitude in this paper.

## 4.1 Mesa-type structure

In this sub-section, mesa-type trench structure (demonstrated on the Fig. 4) is evaluated in terms of acoustic crosstalk reduction. The structure is not expected to be as efficiently as in [6] as far as a  $\lambda/4$  inter device placement cannot be respected due to the high acoustic wavelength in silicon at operating frequency.



Figure 4: Schematics of a mesa-type trench structure used for an acoustic crosstalk reduction

The results of a crosstalk dependence on a mesa depth with fixed width is shown on the Fig. 5. It can be seen that an optimal point which minimizes a coupling amplitude can be found around  $2.5\mu m$ .



Figure 5: Simulated result of a coupling amplitude as a function of depth of mesa trenches with fixed width of 10 µm

Conversely, the impact of mesa width on a crosstalk with fixed depth is depicted on the Fig. 6. This result is different from the previous one and demonstrates that higher width of mesa trench is preferred.



Figure 6: Simulated result of a coupling amplitude as a function of width of mesa trenches with fixed depth of 1 µm

A series of simulations are carried out to evaluate the optimal parameters of mesa trenches dimensions. The results are plotted on the color map on Fig. 7. It has a saddle shape which demonstrates the relation between width and depth impact, with a minimum value of coupling amplitude found to be 0.23nm with trench width of  $20\mu m$  and depth of  $2.5\mu m$ . This result demonstrates that mesa structure can remove up to 89% of crosstalk according to this result.



Figure 7: Simulated result of a coupling amplitude as a function of width and depth of mesa trenches

### 4.2 Ring-shaped pillars

A simple surface ring-shaped pillar structure is analyzed. It is depicted on the Fig. 8. Two cases are considered: silicon oxide pillars machined on the top layer and polymer pillars deposited on the surface.

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Figure 8: Cross-section schematics of a pillar ringshaped structure used for an acoustic crosstalk reduction

## 4.2.1 Silicon oxide pillars

As in the previous sections, a series of simulations is performed to evaluate the coupling amplitude as a function of width and height of silicon oxide pillars. The results are depicted on the Fig. 9. It can be seen that an optimal configuration exists and corresponds to the pillar height of  $20\mu m$  and width of  $15\mu m$  with an evaluated coupling amplitude of 0.44nm, which corresponds to the overall improvement in a crosstalk amplitude reduction of 78%.



Figure 9: Simulated result of a coupling amplitude as a function of width and height of silicon oxide pillars etched on the top of the wafer

### 4.2.2 Polymer pillars

In this case, the pillars are considered to be formed by a polymer deposited on the surface of the wafer. As previously, the study of height and width impact is carried out. The results are shown on the Fig. 10. It can be seen that this acoustic crosstalk reduction solution is not very efficient: the minimal coupling amplitude is found to be equal to 1.84nm, which corresponds to 8% of crosstalk amplitude reduction.



Figure 10: Simulated result of a coupling amplitude as a function of width and height of polymer pillars deposited on the top of the wafer

# 4.3 Mesa-type structure with trenches filled with polymer

Finally, in this section a combination of two solutions is considered. A schematic cross-section of this configuration is depicted on Fig. 11.



Figure 11: Cross-section schematics of a mesa-type structure with trenches filled with polymer used for an acoustic crosstalk reduction

The results of mesa-type structure with trenches filled with polymer dimensions analysis are plotted on the Fig. 12. The pillar height is fixed at the value of  $5\mu$ m. It can be seen that this result is very similar to the one obtained in the section 4.1, and demonstrates that polymer structures are not efficient at acoustic crosstalk reduction in studied problem. Similar to the simple mesa structure, the evaluated optimal value is 0.24nm for a crosstalk amplitude with mesa width of 20 $\mu$ m and depth of 2.5 $\mu$ m, with the crosstalk reduction efficiency estimated to be equal to 88%.

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Figure 12: Simulated result of a coupling amplitude as a function of mesa width and depth, with trenches filled with polymer

# 5. Conclusions

This paper presents a general overview of the modern techniques used to introduce the acoustic isolation in the MEMS-based systems. A general overview of modern techniques for acoustic isolations in MEMS is presented and compared in terms of their applicability to pMUT arrays in terms of their operating frequencies, miniaturizability and crosstalk reduction efficiency.

Several solutions that can be applied to a typical existing pMUT design are considered: mesa-type structure, depositing of polymer ring-shaped pillars and using an additional polymer layer on the back side of the system. These results are compared to the reference case where no acoustic isolation is present. These numerical finding are summarized at Table 2. From presented study it can be concluded that a mesa-type structure, while being relatively simply to design and fabricate, may provide an optimal design solution to reducing the crosstalk by almost one order of magnitude.

| Filter                | Crosstalk     | Crosstalk |
|-----------------------|---------------|-----------|
| configuration         | amplitude, nm | reduction |
| No acoustic isolation | 2             | 0%        |
| Mesa-type structure   | 0.23          | 89%       |
| Silicon oxide pillars | 0.44          | 78%       |
| Polymer pillars       | 1.84          | 8%        |
| Combined solution     | 0.24          | 88%       |
| (Mesa-type +          |               |           |
| polymer)              |               |           |

Table 2. Results summary for different acoustic isolation configurations for pMUT array applications

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