



Optimizing Interface Conductivity in Electronics



The latest eBook from
Advanced Optical Metrology.
Download for free.

Surface roughness is a key parameter for judging the performance of a given material's surface quality for its electronic application. A powerful tool to measure surface roughness is 3D laser scanning confocal microscopy (LSM), which will allow you to assess roughness and compare production and finishing methods, and improve these methods based on mathematical models.

Focus on creating high-conductivity electronic devices with minimal power loss using laser scanning microscopy is an effective tool to discern a variety of roughness parameters.

EVIDENT
OLYMPUS

WILEY

Device-to-Materials Pathway for Electron Traps Detection in Amorphous GeSe-Based Selectors

Amine Slassi, Linda-Sheila Medondjio, Andrea Padovani, Francesco Tavanti, Xu He, Sergiu Clima, Daniele Garbin, Ben Kaczer, Luca Larcher, Pablo Ordejón, and Arrigo Calzolari*

The choice of the ideal material employed in selector devices is a tough task both from the theoretical and experimental side, especially due to the lack of a synergistic approach between techniques able to correlate specific material properties with device characteristics. Using a material-to-device multiscale technique, a reliable protocol for an efficient characterization of the active traps in amorphous GeSe chalcogenide is proposed. The resulting trap maps trace back the specific features of materials responsible for the measured findings, and connect them to an atomistic description of the sample. The metrological approach can be straightforwardly extended to other materials and devices, which is very beneficial for an efficient material-device codesign and the optimization of novel technologies.

1. Introduction

Chalcogenides are the materials of choice for nonvolatile phase-change memory (PCM)^[1–3] and ovonic threshold switching (OTS)^[4,5] selector devices.^[6,7] While for the case of PCM memories good candidate materials have been identified mainly within the family of germanium-antimony-tellurium (GST) compounds (with different stoichiometry^[8,9] and doping^[10,11]), the ideal material for OTS has still to be identified notwithstanding the many candidates proposed so far, such as GeSe, GeS, AsTe, AlTe, TeAsSiGe, SiGeInAsTe, GeTeSbS, and TeAsSiGeP.^[12]

Particularly relevant is the case of amorphous GeSe alloys^[5,13–15]: While a lot of effort has been devoted to GeTe as a prototypical phase change material, and to GeSe₂ as an efficient optoelectronic system, much less is known about the quasi-stoichiometric GeSe compounds. Experimental results indicate that GeSe exhibits ovonic electrical switching in the amorphous phase^[13,16] and that the modulation of the stoichiometry Ge/Se ratio affects the electrical response of the device, in terms of I - V characteristics, threshold potential, switching response, endurance, and power dissipation.^[5] Selectors based on Se-rich GeSe materials exhibit reduced leakage current, improved thermal stability but higher threshold voltage with respect to the Ge-rich counterparts^[17] that, at odd, show superior operation endurance. Despite the huge effort spent on this subject, there is no ubiquitous consensus on the origin of this behavior and on the role of the Ge concentration on the characteristics of selectors.

On general grounds, the research of the optimal electronic material requires a deep understanding of its microscopic properties (especially atomic defects, composition, and doping) and how they determine the electrical characteristics of the final device. While device characteristics such as power consumption, endurance, switching time, or variability are certainly correlated to the properties of the constituent materials, univocal structure–property relations have not yet been unraveled. The origin of this mismatch relies on the fact that experimental measurements, such as I - V and C - V characteristics, are indirect and extensive, which stem from a set of conditions (e.g., temperature, bias, composition, doping) that average and/or compensate in the final results. For these

A. Slassi, F. Tavanti, A. Calzolari
 CNR-NANO Istituto Nanoscienze
 Centro S3, Modena I-41125, Italy
 E-mail: arrigo.calzolari@nano.cnr.it
 L.-S. Medondjio, X. He, P. Ordejón
 Catalan Institute of Nanoscience and Nanotechnology (ICN2)
 CSIC and BIST
 Campus Bellaterra, Barcelona 8193, Spain
 A. Padovani^[†]
 University of Modena and Reggio Emilia
 DIEF
 Via P. Vivarelli, 10, Modena, MO 41125, Italy
 S. Clima, D. Garbin, B. Kaczer
 imec
 Kapeldreef 75, Leuven B-3001, Belgium
 L. Larcher
 AMAT – Applied Materials Italia
 Via Meuccio Ruini 74L, Reggio Emilia, Italy

 The ORCID identification number(s) for the author(s) of this article can be found under <https://doi.org/10.1002/aelm.202201224>.

© 2023 The Authors. Advanced Electronic Materials published by Wiley-VCH GmbH. This is an open access article under the terms of the Creative Commons Attribution License, which permits use, distribution and reproduction in any medium, provided the original work is properly cited.

^[†]Present address: Formerly Applied Materials Italia, Via Meuccio Ruini 74L, Reggio Emilia, Italy

DOI: 10.1002/aelm.202201224

reasons, a coherent protocol able to treat at the same level of accuracy both the materials and the device would be of great value for the comprehension and the further design of the final device.

Prior works exploited density functional theory (DFT) calculations and quasi-Fermi level models^[18,19] to evaluate the electronic structure of the materials, as well as simplified analytical models to estimate the material atomic structure, glass transition temperature, and subthreshold current.^[20] Although providing useful insights, these approaches do not give a comprehensive and self-consistent picture of the device electrical characteristics, since they are not able to represent the *material impact on the device electrical performances*, i.e., the response of the material in the specific device configuration. Indeed, GeSe structure, composition, and trap impact on the device characteristics of OTS selectors remain elusive.

In this work, we propose a multiscale/multiphysics approach that combines experiments, device modeling, and atomistic material simulations to identify the traps in amorphous materials affecting the observed subthreshold conduction of selectors. In particular, we adopt a *device-to-materials* approach that extracts traps/defects characteristics from the experimental device electrical data and connects them to the microscopic properties of the material, in a learning process that correlates the electrical properties of the specific device to the electronic properties of the constituent material. While a few attempts of using this device-to-materials approach have been proposed in the case of crystalline systems,^[21] similar approaches have not been used to unveil the role of defect/traps in amorphous materials, where the lack of long-range order opens to conceptual problems about the structural models and the defect definition, especially for off-stoichiometric systems that do not have a crystalline counterpart.

This methodology is applied to two-terminal metal/insulator/metal (MIM) selectors, based on amorphous $\text{Ge}_x\text{Se}_{100-x}$ films with different thickness and stoichiometry, where $x = \{50, 60\}$ is the percentage of Ge in the sample. While Se-rich GeSe compounds have been largely investigated, even in comparison with the GeSe_2 crystal, here we focus on the case of Ge-rich systems (e.g., $\text{Ge}_{60}\text{Se}_{40}$) for which a crystalline counterpart does not exist. Our results self-consistently explain the experimental current at different temperatures, the conductance data at different frequencies, the changes of material properties as a function of the Ge content and the atomic defects dominating trap-assisted transport. The microscopic analysis of electron trap distribution is eventually carried out in terms of short and medium range order specific of the selected stoichiometry, without invoking ill-defined comparisons with defects (e.g., vacancies) in crystalline systems.

2. Device-to-Materials Approach

The characterization of the material parameters (e.g., composition, thickness, morphology, stoichiometry) that mostly affect the device response is a tremendous task, especially when it deals with the structural and electronic properties of amorphous systems. Even though the computational materials discovery is a major task for the design of new technology, this top-down approach is often infeasible due to economic constraints of the sample's fabrication process. Thus, an alternative solution is to follow the inverse device-to-materials pathway that extracts the intrinsic properties of the materials from the experimental data. Since the electrical characteristics of chalcogenide selectors and their temperature dependence are well explained by a trap-assisted carrier model, the identification of defects/traps becomes crucial in the process.

Here, we developed the three-step approach, summarized in **Figure 1**: Step 1: Experimental setting involves the sample preparation, the device fabrication, and the measurement of the electrical characteristics ($I-V$, $G-V$). Step 2: The experimental output is used as the input of a specific *defect spectroscopy* (DS) functionality, implemented in the device-oriented modeler Ginestra^(R),^[22] which is specifically developed to extract intrinsic material properties and characteristics of the defects (thermal ionization and relaxation energies, distribution within the bandgap) from the electrical measurements. Step 3: The resulting defect mapping is interpreted in terms of ab initio simulations, based on density functional theory.

The core of the process is the DS functionality that relies on the accurate modeling and simulation of the carrier-defect interactions that are at the bases of the defect-assisted trapping and transport (trap-assisted tunneling, TAT),^[23] as described in the framework of the multiphonon theory which accounts for carriers-phonons coupling and lattice relaxation processes^[23-25] (see the Experimental Section). The DS approach comprises four main steps: i) The measured device/structure is recreated within the Ginestra^(R) simulation platform, starting from the known device and material parameters (layers thickness, bandgap, dielectric constant, etc.). ii) The measured experimental data are loaded into the DS tool, as a function of the temperature, voltage, and frequency (the latter for $G-V$ data only). In order to guarantee high accuracy and uniqueness of the results, the DS methodology has to be applied to multiple electrical data (e.g., $I-V$ and $G-V$ as in this paper) measured under multiple conditions (voltages, temperatures, and frequencies). iii) Definition of the material and trap parameters to be extracted as well as their variation range to be considered during the optimization procedure. This information is defined in the input file. iv) An automatic optimization procedure accurately reproduces the experimental data by varying the selected



Figure 1. Sketch of the device-to-materials workflow.

parameters within the specified intervals by minimizing the overall error between the experimental and the simulated electrical characteristics. A modified root mean square error is used for this purpose. At the end of the procedure, the DS module provides the energy-versus-position (E, z) map of the defects responsible for the observed voltage, temperature, and frequency dependence of the experimental data (uniform spatial distributions in the whole GeSe thickness are considered in this work for simplicity). It also provides selected material/device parameters (e.g., work function and energy bandgap) as well as the defects relaxation energy (E_{REL}), which represents the energy required for the lattice relaxation process to occur when the defect state changes its charge state.^[23–25] E_{REL} is estimated from the temperature dependence and determines the charge capture/emission time constants and their temperature dependence and it is linked to the defects' atomic structure. Therefore, it can be used, together with the extracted thermal ionization energy band, to identify the nature of the defects responsible for the observed electrical data.^[23]

3. Results

3.1. Step 1: Device Fabrication and Experimental Data Generation

Three different Ge_xSe_{100-x} OTS layers were prepared and integrated in two-terminal MIM selectors, by combining materials with different thicknesses (10 and 20 nm) and Ge concentration ($x = 50$ and 60) as reported in Table 1. The amorphous Ge_xSe_{100-x} films were prepared by room temperature physical vapor deposition (PVD). The layers were integrated into TiN/GeSe/TiN two-terminal structures in a 300 nm process flow and passivated with a low-temperature back end of line process. The details of fabrication and measurement sets are reported in Section S1 (Supporting Information). Each current–voltage (I – V) or conductance–voltage (G – V) measurement was performed on a fresh device, and multiple (at least three) sized devices were measured at each measurement condition.

3.2. Step 2: Device Modeling and Trap Mapping

The proposed DS technique is applied to the I – V and G – V data measured on the GeSe samples D21 and D22, described above; results for the D16 system are reported in Section S1 (Supporting Information). Since the conductance is found to be independent of the frequency, only the data measured at 2 kHz are considered for the DS procedure.

Table 1. MIM capacitors based on Ge_xSe_{100-x} with varying Ge content x and varying chalcogenide thickness. Data about D16 sample are reported in the Supporting Information.

Sample ID	M/I/M capacitor
D16	TiN/10 nm $Ge_{60}Se_{40}$ /TiN
D21	TiN/20 nm $Ge_{50}Se_{50}$ /TiN
D22	TiN/20 nm $Ge_{60}Se_{40}$ /TiN

GeSe has a small bandgap from ≈ 0.8 to ≈ 1.5 eV depending on the stoichiometry, which is indicative of a small Schottky-like energy barrier at the TiN/GeSe interface (barrier larger than the bandgap would correspond to an Ohmic-like contact). The resulting electrical characteristics for devices D21 and D22 are shown in Figure 2a,b and c,d, respectively. An excellent agreement between experiments and simulations is obtained for both stacks in the whole range of voltages and temperatures considered. The consistency of the applied model and of corresponding results has been checked against the thickness change, by comparing the data for devices D22 and D16 (see Figures S1 and S2, Supporting Information).

Starting from the I – V and G – V characteristics, we extracted the material and trap parameters, as reported in Table 2. The Ge-rich system has a lower bandgap (1.1 eV) than the stoichiometric one (1.45 eV), in agreement with previous works^[26,27] and consistent with the reduction of the experimental gap observed in the D21 device. The DS defect maps reported in Figure 3 show for both compositions the presence of two trap bands: one for holes and one for electrons. In both cases, trap distributions remain spatially uniform through the thickness of the sample. The trap density (N_{T_i}) is similar for electrons and holes, and it is higher for the Ge-rich case than stoichiometric one.

However, important differences can be detected in the two samples. In the case of $Ge_{50}Se_{50}$ trap bands are slightly narrower and located on average at ≈ 0.4 and ≈ 0.2 eV from the valence and conduction band edge, respectively. The $Ge_{60}Se_{40}$ compound is characterized by broader trap bands located on average at ≈ 0.1 and ≈ 0.3 eV from valence band top and the conduction band bottom. At zero bias traps are either fully occupied or empty, with the Fermi level lying in the middle of the gap. Once the external bias is applied, both carrier types contribute to conduction, with a net prevalence of electron conduction through the upper trap band (n-type). However, conduction type may depend on structural parameters of the device, such as thickness, effective bandgap, electrode work-function. Indeed, the analysis for the D16 sample exhibits an equal contribution of both hole and electron carriers.

In order to investigate the role of traps in the transport mechanisms within the GeSe layer, we recalculated the I – V characteristics of the D22 sample, by selectively deactivating specific scattering terms (Figure 4a). In particular, we considered the electrical characteristics resulting i) without the inclusion of the defect contributions, ii) without the trap-to-trap transitions, and iii) without the trap-to-band transitions. The results at different temperatures for the D22 sample are summarized in Figure 4b–d. The absence of trap contributions [panel (b)] imparts a reduction of the simulated current (dashed lines) by one order of magnitude over the entire voltage range [–2 V, +2 V], and a remarkable worsening of the agreement with the experimental data (circles), only partially compensated by the increase of the temperature. The suppression of the trap-to-trap [panel (c)] or of the trap-to-band [panel (d)] contributions has smaller effects on the I – V characteristics: trap-to-trap seems more relevant at low bias ($V < 1.0$ eV), while trap-to-band has a major effect for higher bias ($V > 1.5$ eV). We can conclude that the most relevant contribution for current stems from the presence of the trap bands within the bandgap, while the

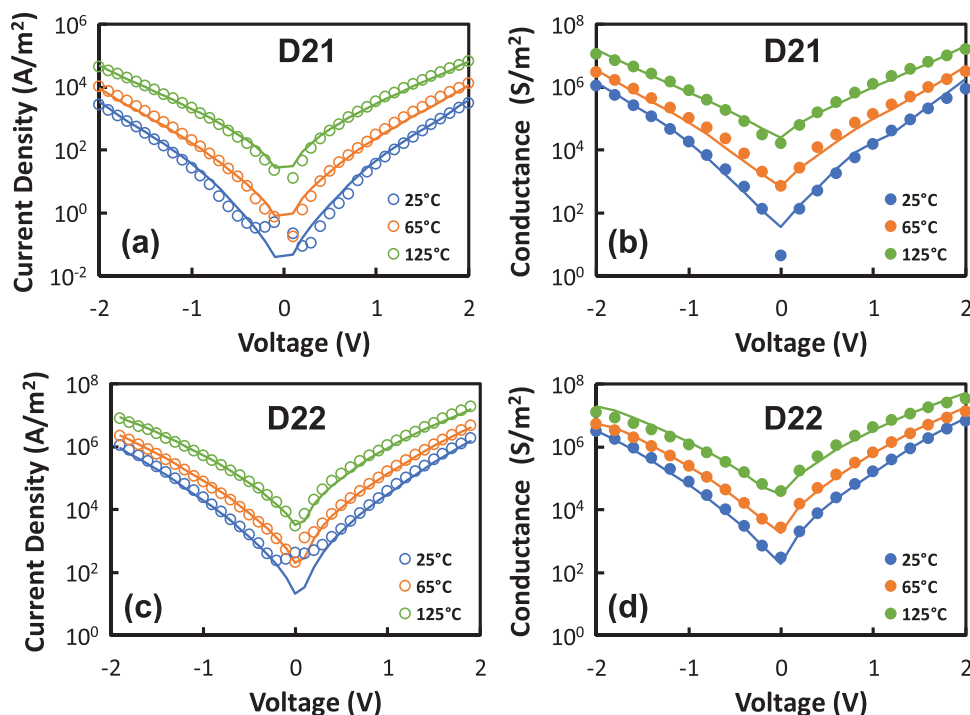


Figure 2. a, c) I - V and b, d) C - V characteristics of 20-nm-thick TiN/Ge₅₀Se₅₀/TiN (D21, top row) and TiN/Ge₆₀Se₄₀/TiN (D22, bottom row) capacitors, at different temperatures. Dots (lines) correspond to measured (simulated) data.

other mechanisms are responsible for the specific details of the resulting electrical characteristics. This can be justified in terms of the simultaneous small bandgap and high trap density of both systems that make trap-to-trap and trap-to-band energetically similar.

Another important parameter that can be extracted through the DS tool is the relaxation energy (E_{REL}), which is directly connected to the atomic nature of the trap and is typically derived from the temperature dependence of the charge transport (i.e.,

Table 2. Material and trap parameters extracted by using the DS utility to reproduce I - V and G - V data measured, as a function of temperature, on the different OTS devices. E_G is the (mobility) bandgap, t_{GeSe} is the film thickness, $E_{T,i}$ and $\Delta E_{T,i}$ are average and spread of the traps thermal ionization energy, $E_{REL,i}$ is the traps relaxation energy, and $N_{T,i}$ is the traps density for electrons ($i = el$) and holes ($i = h$), respectively.

Parameter	Ge ₅₀ Se ₅₀ D21	Ge ₆₀ Se ₄₀ D22
E_G	1.45 eV	1.10 eV
$E_{T,el}$	0.23 eV	0.34 eV
$\Delta E_{T,el}$	0.15 eV	0.2 eV
$E_{REL,el}$	0.15 eV	0.4 eV
$N_{T,el}$	$5 \times 10^{18} \text{ cm}^{-3}$	$2.48 \times 10^{19} \text{ cm}^{-3}$
$E_{T,h}$	1.1 eV	0.9 eV
$\Delta E_{T,h}$	0.15 eV	0.2 eV
$E_{REL,h}$	0.15 eV	0.3 eV
$N_{T,h}$	$5 \times 10^{18} \text{ cm}^{-3}$	$2.48 \times 10^{19} \text{ cm}^{-3}$
t_{GeSe}	21.3 nm	20.5 nm

of the leakage current, as shown in Section S1, Supporting Information). However, an accurate extraction of this key parameter is possible in principle only if the observed current-temperature dependence originates from purely trap-assisted transport. In the case of the thick, small bandgap GeSe layers considered here, this could be not the case and other mechanisms, such as tunneling from/to the electrodes may play a role: simulations show that the charge transport occurs by means of carrier excitation at the metal TiN/GeSe interface and then through the defect band by means of trap-to-trap transitions. Since both processes (excitation and trap-to-trap) are strongly dependent on the temperature, the E_{REL} value of 0.1–0.5 eV for both electrons and holes that is consistently extracted for the different GeSe samples has to be verified also with other methods (e.g., ab initio calculations).

The extracted trap density is consistent among samples and within the range typically reported in the literature. Besides characterizing the properties and distribution of GeSe traps, the DS tool also allows the extraction of the GeSe effective thickness (t_{GeSe}). As reported in Table 2, the extracted value of t_{GeSe} is very consistent for both stoichiometries and in very good agreement with the nominal thickness.

This analysis demonstrates the DS capability to characterize the trap distribution of samples, and to discern among different compositions, temperatures, and geometrical properties of the device. However, the DS tool is not sufficient to unravel the structural properties and chemical origin of those traps. For crystalline systems, the analysis of defect states is routinely done by considering the effects of point defects (e.g., vacancies, interstitial, impurities) that usually cause the appearance of localized states in the semiconductor band gap. In the present

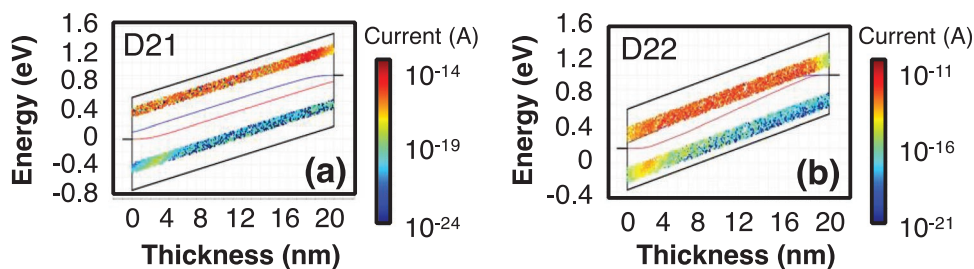


Figure 3. Defect band extracted by applying DS on the data in Figure 2 for (a) D21 and (b) D22 device, at $V = -1.0$ V. Colored dots represent point defects, the side color scale indicates the current driven by each trap. Horizontal gray lines mark the Fermi level of the two external leads (i.e., the applied bias), the red and blue lines across the sample are the calculated quasi-Fermi level for holes and electrons, respectively.

case, the amorphous structure of GeSe prevents from using this intuitive approach and demands for an extensive microscopic investigation of realistic disordered structures.

3.3. Step 3: First Principles Calculations

In order to interpret the DS results from a microscopic point of view, we performed a high throughput study of the structural and electronic properties of GeSe compounds based on first principles calculations. Given the intrinsic disordered nature of the amorphous materials, the understanding of the interplay between local-order structures and electronic trap states requires the capability of treating large systems at the atomistic level and of having access to the corresponding electronic structure. In the absence of direct experimental results about the atomic structure of the glassy phase at room temperature, the identification of a reliable geometry is a major challenge for theory. In order to obtain realistic models of the amorphous

structure without resorting to experimental information or adjusted interatomic potentials, we carried out an extensive set of independent simulations performed with different multiscale techniques and different codes, including molecular dynamics (MD) using interactions from both classical potentials (LAMMPS code^[28]) and density functional theory (SIESTA^[29,30] and QuantumEspresso^[31]).

As a starting point, we considered the atomic model of $\text{Ge}_{50}\text{Se}_{50}$ resulting from classical MD simulations on a large simulation cell including 4480 atoms (labeled *big cell*). The structure is obtained through nanosecond-long melt-and-quench cycles (5 K ps^{-1} cooling rate), following the procedure described in ref. [32]. This approach allows the system to rearrange at the local- and medium-order structures avoiding unphysical “frozen-liquid” structures, and it has been demonstrated to generate a realistic model for the amorphous GeSe compound.^[32,33] The increase of Ge content from 50% to 60% in $\text{Ge}_x\text{Se}_{100-x}$ causes a systematic change of the average coordination number of Ge and Se species that moves from 3:3 (as

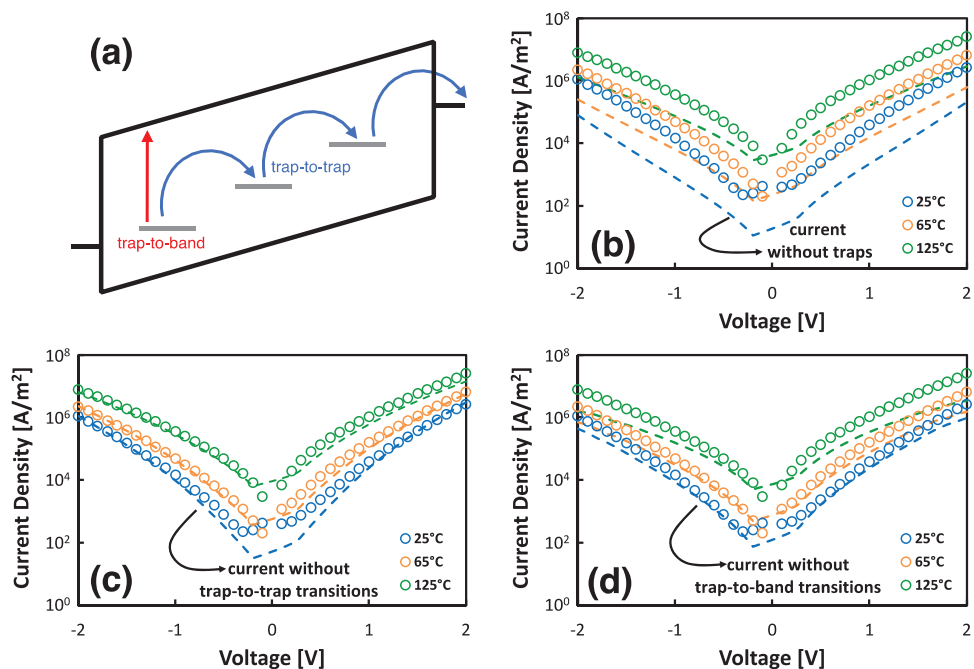


Figure 4. a) Graphical sketch of relevant transport mechanisms assumed in the DS model. Simulation of I - V characteristics of D22 sample at different temperatures, by selecting specific transport mechanism: b) without traps, c) without trap-to-trap transitions, and d) without trap-to-band transitions. Dots (lines) correspond to measured (simulated) data.

in the crystal) to a phase where Ge has a mixed fourfold (as in Ge-bulk) and threefold coordination. The different short-range structures and their spatial arrangement in the medium range are expected to have a critical role in the formation/appearance of the electrical trap states (see below). We assumed the *big cell* structure as the reference testbed for the further comparisons.

We first exploited the capability of the SIESTA code to treat large systems by simulating one 4480 atoms structure extracted from the MD simulation at 300 K (i.e., one frame from MD trajectory). The structure was fully relaxed using the DFT forces computed in SIESTA, through a total-energy-and-force optimization approach to allow the system to readjust the atomic distances and angles on the basis of the DFT potential. The comparison between the original MD and the DFT results is summarized in **Figure 5a–c**, which reports the partial radial distribution function $g(r)$ of the (a) Ge–Ge, (b) Ge–Se, and (c) Se–Se pair distances. Except for the expected minor differences due to bond-length optimization, the optimized DFT results (labeled DFT *big cell*) well reproduce the classical ones (labeled MD *big cell*).

However, a meaningful analysis of an amorphous system requires the investigation of several frames to gain a statistical average of the structural properties. This makes the use of such a large cell practically unfeasible for extensive DFT simulations. This opens the problem on how to generate smaller atomic models that are simultaneously structurally reliable and numerically affordable. We generated two independent sets of structures, by exploiting two different approaches and DFT codes.

In the first one (labeled *m&q*) we performed a sequence of melting-and-quenching cycles on model structures, by using the SIESTA code. Initial Markov chain Monte Carlo simulations have been used to generate the disordered starting structures of $\text{Ge}_x\text{Se}_{100-x}$ ($x = 50, 60$), with $N = 336$ atoms in a cubic box

of side $L = 21.0$ Å. Starting from these initial structures, the amorphous models have been obtained through melting-and-quenching cycles, carried on by using ab initio MD. The quenching step is particularly critical to obtain trustable amorphous results. Preliminary results (see Section S2, Supporting Information) indicate that i) the relaxation of volume under constant pressure, ii) the use of a relatively large basis set (in particular, double- ζ plus polarization [DZP]^[34]) during the quenching procedure, and iii) a low quenching rate are necessary steps for the building of realistic GeSe amorphous structures. The details of the quenching-and-melting approach are summarized in **Table 3**. Further computational details and accuracy tests are reported in the Experimental Section and in the Supporting Information.

In the second approach (labeled *cut*), we extracted from the original MD trajectories on the large (4480 atoms) $\text{Ge}_x\text{Se}_{100-x}$ ($x = 50, 60$) models, 12 smaller cubic boxes containing 500 atoms each, with the same stoichiometries and densities of the parent MD systems. These smaller cells (cut from the larger box and imposing periodic boundary conditions) were relaxed using classical MD at a constant temperature of 300 K for 10 ns, in order to allow the broken bonds at the borders to rearrange and match the new periodic boundary conditions. Each system has been further optimized at the DFT level, by using the QuantumEspresso code^[31] (see the Experimental Section and Supporting Information for further details).

The averaged radial distribution functions of model structures resulting from the *m&q* and *cut* approaches are shown in **Figure 5d–f**. A direct comparison clearly indicates that the final configurations resulting from the two approaches resemble the original *big* one, in particular the radial distribution functions are very similar to the original results (**Figure 5**, top panels). This confirms that we generated atomically different

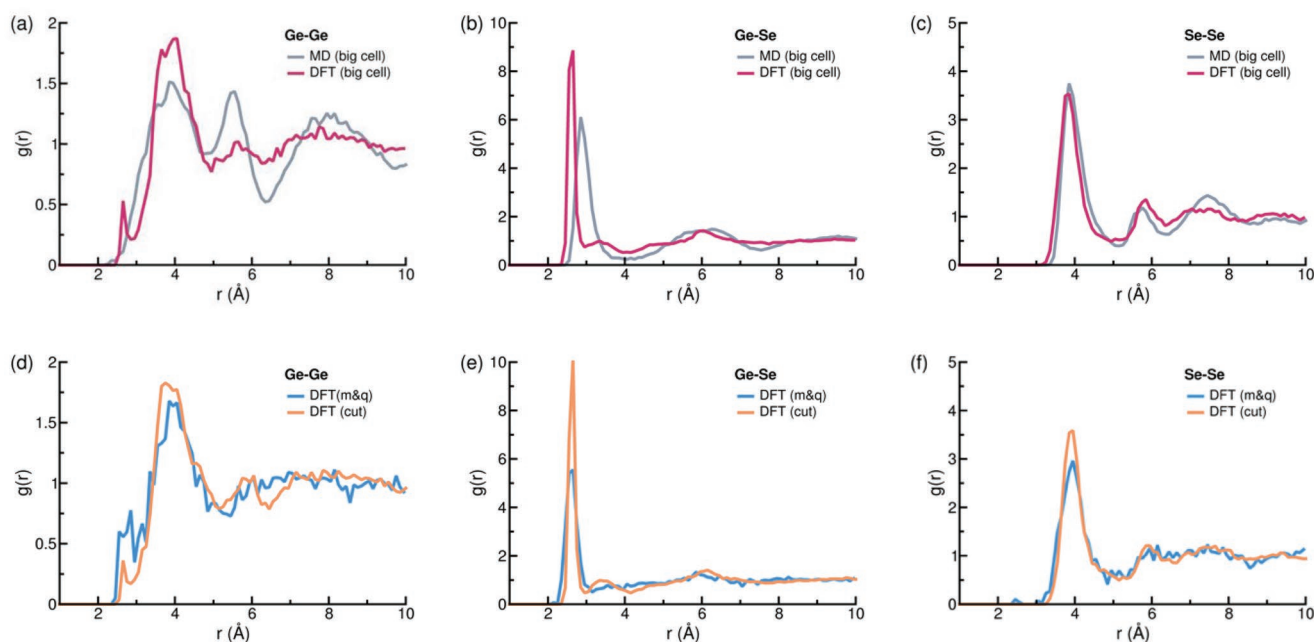


Figure 5. Radial distribution functions $g(r)$ for a,d) Ge–Ge, b,e) Ge–Se, c,f) Se–Se pair distances of $\text{Ge}_{50}\text{Se}_{50}$, simulated through different techniques. Results reported in the top layer are relative to the 4480-atom system (*big cell*); results in the bottom layer correspond to smaller cells (*m&q* and *cut*).

Table 3. Melt-and-quench ($m\alpha q$) protocol for generating the amorphous structures. SZ and DZP indicate single- ζ basis and double-polarized- ζ basis sets; NPT indicates isothermal–isobaric ensemble.

Initial structure	Generate initial structure with MCMC (336 atoms). Then relax structure (cell and atom coordinates)
Melt	6 ps at 1200 K, SZ, NPT + 6 ps at 1200 K NPT
Quench 1	1200–800 K, 3 ps in total, 1 fs per step, $\tau_{\text{relax}} = 1000$ fs; SZ, NPT
Quench 2	800–400 K, 3 ps in total, 1 fs per step, $\tau_{\text{relax}} = 750$ fs; SZ, NPT
Quench 3	400 K, 3 ps in total, 1 fs per step, $\tau_{\text{relax}} = 750$ fs; DZP, NPT

but statistically equivalent systems, whose structural characteristics are independent from the procedure and the numerical details used to realize the model. Similar considerations hold for the corresponding electronic structures, even though in this case the specific choice of the numerical details (e.g., exchange–correlation functional) may affect the quantitative value of parameters such as the bandgap. However, as evident from the comparison of the electronic density of states (DOS) reported in Figure S8 (Supporting Information), both DFT approaches give the same qualitative results. This assures that the trap states analysis is robust and independent from the specific choice of the used DFT code and functional.

On the basis of these results, we performed a set of calculations to generate model structures for the two different stoichiometries corresponding to the experimental samples D21 and D22, i.e., $\text{Ge}_{50}\text{Se}_{50}$ and $\text{Ge}_{60}\text{Se}_{40}$. The Se-rich counterpart $\text{Ge}_{40}\text{Se}_{60}$ has also been simulated for comparison (see the Supporting Information). For all GeSe compounds, several (12) atomic structures have been generated to have a statistical meaningful insight of the amorphous characteristics. The complete set of DOS plots for each simulated structure is summarized in Figures S9–S11 (Supporting Information). The electronic properties of the three stoichiometries have similar spectral features: the valence DOS plots are characterized by three distinctive peaks (labeled A, B, and C in Figure S12, Supporting Information) in agreement with recent X-ray spectroscopy.^[35] Peak C corresponds to the energy region going from –14 to –12 eV and has mostly a Se(4s) character. The central

region, from –10 to –6 eV (peak B), is dominated by the Ge(4s) contribution, while the region from –5 to 0 eV (peak A) has a mixed Se(4p)–Ge(4p) character, with a major contribution from Se states, which varies with the Ge/Se ratio. The lowest energy part of the conduction band has a mixed Ge–Se behavior as well, but here the major contributions derive from the Ge atoms, as a function of the composition. This characteristic peak distribution is typical of Ge–Se compounds, such as crystalline GeSe and GeSe_2 .^[36–38] Despite these overall common aspects, the gap regions, which are the most relevant for the transport properties, exhibit different characteristics depending on the stoichiometry.

Figure 6 shows the DOS plots resulting from the ensemble average on the 12 structures of $\text{Ge}_{50}\text{Se}_{50}$ and $\text{Ge}_{60}\text{Se}_{40}$, respectively, as resulting from the *cut* approach. A Hubbard-like potential has been included to both Ge and Se atoms, in order to correct the severe underestimation of the bandgap due to standard DFT approach (see Section S2, Supporting Information, for further details). The zero-energy reference is set to the corresponding Fermi level. Red diamonds indicate the inverse participation ratio (IPR) of single particle orbitals,^[39,40] which is a measure of the spatial localization of the electronic states: the higher the IPR the more localized the states.

The mobility gap E_g is determined as the energy-level border separating the localized and extended bands, i.e., as the energy region where the IPR increases with respect to the deep valence/conduction bands. Increasing the Ge amount in the material, the mobility gap E_g decreases from 1.4 eV ($x = 50\%$) to 0.7 eV ($x = 60\%$) in agreement with the experimental data and the DS results. The density, the localization and the energy distribution of defect states in the gap strongly depend on the stoichiometry. The nature of the mobility-gap states is found to be different from the Valence-Alternating-Pairs (VAP) model for Se-rich GeSe.^[41–43] In the case of $\text{Ge}_{50}\text{Se}_{50}$ [panel (a)], the gap is almost empty with a few defect states that accumulate close to the edges of the bandgap, forming two trap bands (dashed cyan lines) for holes and for electrons, in agreement with the DS results of Figure 3. The states forming these defect bands are spatially localized (Figure 7a,b) but involve different local coordination depending on n-type or p-type character of the trap. A similar picture also holds for the Se-rich $\text{Ge}_{40}\text{Se}_{60}$ compound (Figure S13, Supporting Information),

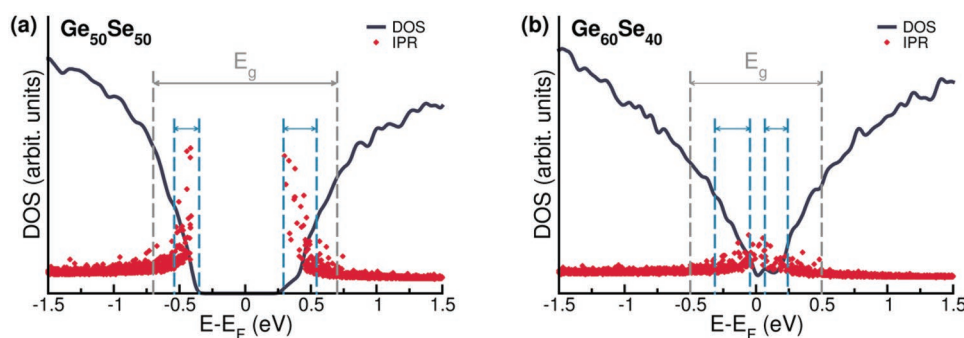


Figure 6. Averaged density of states (DOS) plot for amorphous GeSe compounds at different stoichiometries. Red diamonds indicate the inverse participation ratio (IPR) of single-particle states. Vertical gray (cyan) dashed lines mark the mobility gap E_g (trap bands) of the system. The zero-energy reference is set to the Fermi level of each system.

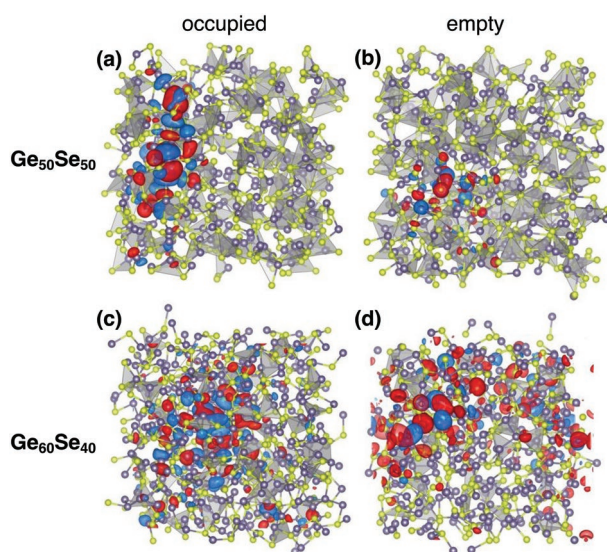


Figure 7. Single-particle charge-density plots of selected defect states in the mobility gap of a,b) $\text{Ge}_{50}\text{Se}_{50}$ and c,d) $\text{Ge}_{60}\text{Se}_{40}$ for occupied (left) and empty (right) states. Gray polyhedrons indicate ordered threefold and fourfold local-order structures centered on Ge atoms. Ge (Se) atoms are colored in purple (green), respectively. Red (blue) isosurfaces indicate the positive (negative) sign of the single-particle wavefunction.

even though the defect states are more distributed across the bandgap, giving rise to broader trap bands. More complex is the case of $\text{Ge}_{60}\text{Se}_{40}$ (Figure 6b): even though two trap bands are still visible, the defect states are largely spread within the energy gap. This agrees with the appearance of the broader defect bands in Figure 3b. However, in this case the in-gap states are spatially delocalized all over the cell (Figure 7c,d), so they cannot properly be considered as trap states, but rather as conductive states. This goes in parallel with the enhancement of the current measured in sample D22 (Figure 2).

Figure 7 shows the single-particle charge density plots of a few selected defect states of the GeSe systems for both occupied (a, c) and empty (b, d) states. Gray polyhedrons indicate ordered threefold and fourfold local-order structures centered on Ge atoms, that are among the most frequent local arrangements found in these systems.^[33] From the analysis of the spatial distribution of the in-gap states, we draw a few important observations: i) at odd with the usual crystalline case, the localized states of these amorphous structures are not localized around the single atomic site (e.g., the point defect), but involve a few bonds. ii) The defect states often appear at interconnections among a few threefold and fourfold polyhedrons, indicating a key role of Ge coordination on the formation of trap states, in agreement with previous theory reports.^[38] iii) In the stoichiometric case [panels (a) and (b)], the abundance of Se favors the formation of Ge–Se bonds arranged in ordered local structures (local order). This justifies the presence of the reduced number of trap states in the $\text{Ge}_{50}\text{Se}_{50}$ system. On the contrary, the deficiency of Se in the Ge-rich case causes the formation of homopolar Ge–Ge bonds, which tend to be interconnected across the cell instead of organizing in folded structures (i.e., higher delocalization). Note that the number of polyhedrons decreases as the Ge content increases. iv) Since the top of the

valence band (peak A, Figure S12, Supporting Information) has a prevalent Se character, for all stoichiometries (including $\text{Ge}_{60}\text{Se}_{40}$) hole defects have a mixed Ge–Se character, which cannot be simply ascribed to the presence or the absence of a single element, as for the case of point defects in crystals. While the concept of vacancy or species-deficiency may heuristically be extended to the case of the stoichiometric compound ($x = 50\%$) in view of the comparison with the crystalline counterpart, the same concepts are completely ill defined in the case of off-stoichiometric systems. Indeed, the 20% of compositional unbalance as in $\text{Ge}_{40}\text{Se}_{60}$ or $\text{Ge}_{60}\text{Se}_{40}$ compounds causes an overall modification of the interatomic bonding (bond atoms, bond length, bond angles) and not simply a local perturbation next neighbor to the defect.

From this analysis we conclude that: 1) defect states have an interbond spatial distribution of >1 nm; 2) they always have a partial Se contribution; and 3) Ge coordination plays a crucial role in the formation of defect states. Hole and electron defects have different characteristics: the former do not exhibit the presence of homopolar Ge–Ge bonds that are, instead, a common feature of the latter. All electron defect states involve Ge–homopolar bonds. However, not all the Ge–Ge bonds give rise to a state in the gap. The question is: what is the origin of these states? The answer stems from the structural analysis of the medium range order of the system, and in particular from the formation of Ge–Se complexes with different coordination.

To illustrate this point, we considered the trap states relative to a single snapshot of $\text{Ge}_{50}\text{Se}_{50}$, assumed as a testbed of the system. Similar arguments can be extended to the other GeSe cases. We focused on one occupied (T_h) and one empty (T_{el}) trap state, which decorate the band edges of the system (Figure 8), and we analyzed the coordination of the atoms closely involved in these trap states. The occupied defect state (T_h) is localized around three close Ge atoms that are not forming homopolar bonds, but that are simultaneously bonded to Se atoms having different coordination, namely, threefold (III) and fourfold (IV) Se atoms. This Se-coordination mix results in the formation of an unbalanced and localized charge distribution around the Ge atoms that causes the appearance of the trap state in the gap. The T_{el} state is localized across a triple Ge–Ge–Ge chain, where one Ge (the one on the right) is bonded to three inequivalent Se atoms, having II, III, and IV coordination, respectively. Again, the trap state originates from the off-coordination of the bonded Se atoms, and not from the Ge itself or from the formation of the Ge–homopolar bonds. To confirm this statement we analyzed an extended state (labeled *No trap* in Figure 8), where we identified the formation of a Ge–Ge bond. However, both Ge are bonded to homo-coordinated Se atoms (Se^{III}). This prevents the localization of charge and the formation of a trap state, in agreement with extended machine learning investigations on GeSe alloys.^[44] We can conclude that trap states originate from local electron under/over-coordination, in agreement with previous general statements.^[18,38,44] Yet, our results indicate that off-coordination is not associated to a single atomic site as usually suggested, but rather to nm-extended complexes (medium range) that include both chemical species. The multielement/multibond complexity of the trap origin justifies the different interpretations appeared in the literature, often based

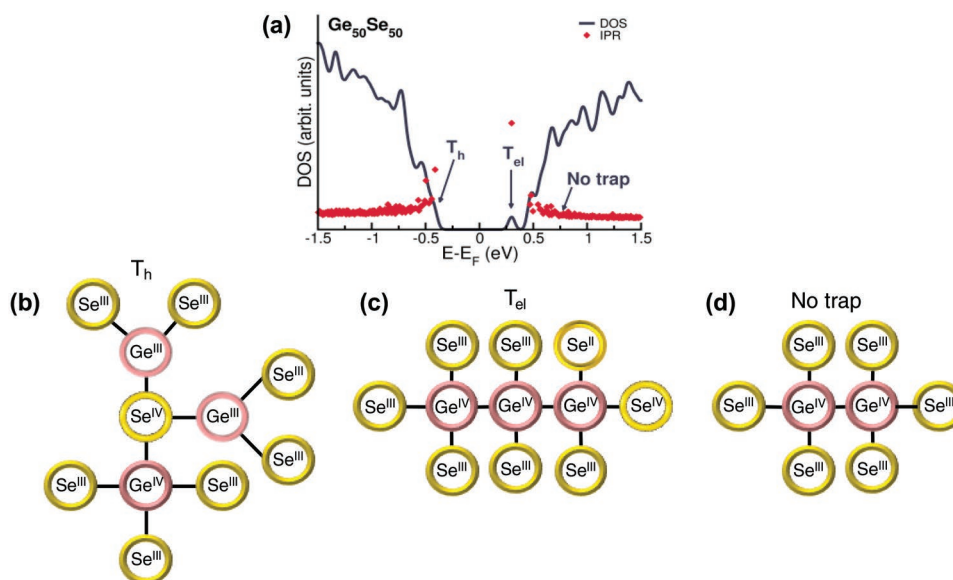


Figure 8. a) Density of states and IPR of a representative snapshot of the $\text{Ge}_{50}\text{Se}_{50}$ system. Labels mark the energy positions of the selected states of panels (b)–(d). b,c) The atomic coordination pattern of the hole (T_h) and electron (T_{el}) trap states, respectively. d) An extended state (No trap) is included for comparison.

on simplified structural models, such as homopolar bonds or folding of single chemical elements (e.g., Ge-fold structures).

Since trap-assisted transport implies the hopping of charge to/from localized states and the 0/−1 (0/+) charge-state transition for electron (hole) carriers, we investigated the effect of including an extra charge to the system. For each initial snapshot of the $\text{Ge}_{50}\text{Se}_{50}$ system, we simulated the case of an extra hole or an extra electron in the unit cell. We fully relaxed the charged structures and we averaged the resulting electronic structures, as summarized in **Figure 9**.

Even though the two characteristic trap bands of Figure 3 are still visible, the inclusion of the extra charge in the system imparted a redistribution of the defect energy within the bandgap that results more occupied with respect to the neutral case. We observe also an increase of the average IPR of the defect states, which corresponds to an increase of the spatial charge localization favored by the local structural relaxation (i.e., polaron-like effect). The direct evaluation of the charge-state transition energies for amorphous systems is an open

challenge within the solid-state community, and goes beyond the aim of this work.

4. Conclusion

We presented a multiscale device-to-materials approach for the identification and the analysis of the trap distribution in electronic devices. Starting from the experimental electrical characteristic of two-terminal TiN/a-GeSe/TiN selector devices, we fully investigated the properties of the active material at the microscopic level and its transport response. This approach allowed us to connect the performances and reliability of the experimental devices to the composition and the stoichiometry of the system. In particular, we unravel the role of charge traps in amorphous chalcogenides and the relation of these defect states with local order of the system. The proposed D2M protocol is general and it can be applied to any CMOS, logic or memory device, independently from the

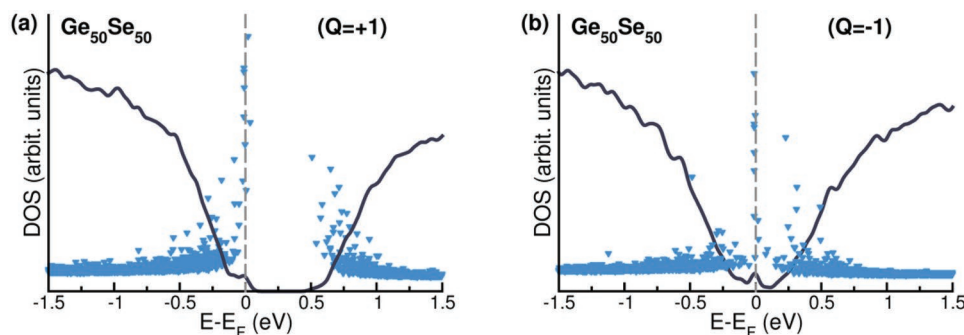


Figure 9. Averaged DOS and IPR plot for $\text{Ge}_{50}\text{Se}_{50}$ in the inclusion of a) an extra hole ($Q = +1$) and b) an extra electron ($Q = -1$) in the simulation cell, where Q is the total charge of the system. Vertical dashed line marks the Fermi level of the system, assumed as the zero-energy reference.

structure, the composition, and the chemical species of the active materials.

Our learning approach combines characterization and metrology aspects that may accelerate the optimization of material-design capabilities by exploring new materials and new compounds, starting from the desired electrical performances of the device.

5. Experimental Section

Fabrication and Characterization: Amorphous $\text{Ge}_x\text{Se}_{100-x}$ films were prepared by room-temperature PVD. TiN/GeSe/TiN MIM capacitors were integrated in a 300 nm CMOS process flow. Electrical characterization was performed with a Keithley 2536 SMU and an Agilent E4980A LCR meter. For further details see Section S1 (Supporting Information).

Device Modeling: Device simulations were performed with the Ginestra software package (Ginestra), a multiscale simulation platform that self-consistently describes all the mechanisms relevant for logic,^[23,45,46] and memory devices.^[47,48] Non-defect-related conduction mechanisms such as direct/Fowler–Nordheim tunneling, thermionic emission, and drift were considered consistently with defect-assisted charge trapping and transport (TAT) model that controls the conduction in a wide variety of materials.^[23,45,49,50] TAT is described in the framework of the multiphonon theory accounting for carriers–phonons coupling and lattice relaxation processes, which depends on the atomistic structure of the defect and is described by the relaxation energy parameter (EREL). Hydrodynamic transport, current-induced power dissipation and temperature increase, distortion and breakage of atomic bonds promoted by field, temperature and electron injection,^[46] diffusion of atomic species,^[47] ferroelectric switching,^[48] and phase change were also self-consistently considered, thus enabling the modeling of material modifications associated to the electrical stress and specific material properties. Calculations were performed considering the local potential given by the applied bias and the defect charge state and occupation. Such a comprehensive and self-consistent description of carriers transport and trapping mechanisms is at the basis of the DS technique^[21] that was developed for extracting defects and properties of material used in modern and novel logic and memory devices.

Materials Simulations: Classical MD Simulations: The $\text{Ge}_{50}\text{Se}_{50}$ system was simulated by using a classical molecular dynamics approach, according to Tavanti et al.^[32] The system contains 4480 atoms and the force field used is based on the well-established Vashishta potential employed for the description at the atomistic level of GeSe_2 or InP systems.^[51] The classical MD simulations were carried out by using the LAMMPS package.^[28] The complete description of the adopted model and the analysis of the classical MD results were reported elsewhere.^[32,33]

Melting-and-Quenching Approach: Various random supercells with Ge:Se coordination number 4:2 and 3:3 were constructed by a Markov Chain Monte Carlo method. In the generation of the structures, a repulsive interaction between atoms was included to make their distribution more homogeneous in the cell, and potential which depends on the Ge and Se coordination, to impose a Ge:Se coordination of 4:2. The resulting geometry was then relaxed prior to the melting-and-quenching procedure by using a conjugate gradient algorithm. Ab initio molecular dynamics (AIMD) were then carried out within the isobaric–isothermal (NPT) ensemble at zero external pressure with an annealing algorithm, by rescaling the velocities and the pressure at each time step of $\Delta t = 1$ fs and the characteristic relaxation time of τ_{relax} . The simulations started by equilibrating each initial configuration at 1200 K for 12 ps, at which the system melts and loses the memory of the initial configuration. After equilibration, each system was quenched to 800 K over a period of 3 ps; this corresponds to an average cooling rate of 133 K ps^{-1} . During the melting and the quenching to 800 K procedure, the τ_{relax} was set to 1000 fs. The obtained configuration at 800 K was quenched

again to 400 K in 3 ps. At this temperature, the structure was equilibrated additionally for 3 ps using now the DZP and a τ_{relax} of 750 ps.

The relaxation and AIMD calculations were carried out with the SIESTA code.^[29,30] SIESTA employs local basis sets, and norm-conserving pseudopotentials from the pseudo-dojo dataset,^[52] in which the Se and Ge 4s and 4p electrons are considered as valence states. The generalized gradient approximation (GGA)–Perdew–Burke–Ernzerhof (PBE)^[53] functional was used to describe the exchange–correlation. DRSLM van der Waals correction was used in all calculations.^[54] Single- ζ (SZ) basis functions^[34] were used for the initial relaxation and finite temperature molecular-dynamics simulation, except in the last AIMD run in which DZP basis functions were used. The basis set was generated with an orbital-confining scheme with a cutoff radius corresponding to the energy shift of 0.001 Ry.^[34] Only the Γ -point was used for the Brillouin zone integration. The convergence criterion for the geometry optimization was set to a maximum force of 0.04 eV \AA^{-1} on each atom. Extended accuracy test on the choice of the basis set is reported in Section S2 (Supporting Information).

Cell Cutting (Cut) Approach: For each stoichiometry, 12 atomic structures of $\text{Ge}_x\text{Se}_{100-x}$ each including 500 atoms were generated. The initial geometries were extracted from the large (4480 atoms) structures generated with the classical interatomic potentials, discussed above. Each structure was then optimized by using a total energy and forces approach based on DFT calculations, as implemented in the QuantumEspresso package.^[31] The electron–ion interactions were treated within the projector augmented wave (PAW) framework. Single particle wave functions and charge were expanded in planewaves up to energy cutoff of 30 and 300 Ry, respectively. The exchange–correlation functional was described by the vdW-DF2-b86r exchange–correlation functional^[55] that takes into account the van der Waals contributions which are known to play a crucial role in amorphous chalcogenides.^[56] All the atomic positions were allowed to move until the maximum ionic forces were less than 0.002 eV \AA^{-1} . Integrations over the Brillouin zone were sampled at the Γ -point only. In order to overcome the well-known bandgap underestimation of DFT, a DFT+ U correction was included to the electronic structure, within the pseudohybrid Hubbard density functional approach (namely ACBN0^[57]) at single point scf-calculations. The resulting Hubbard-like values are: $U_{\text{Ge}} = 0.16$ eV and $U_{\text{Se}} = 2.28$ eV. The detailed analysis of the effect of the inclusion of the Hubbard- U potential in comparison with the hybrid HSE06 approach is reported in the Supporting Information.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

Acknowledgements

A.S. and L.-S.M. contributed equally to this work. This work was supported in part by EC through H2020-NMBP-TO-IND project GA no. 814487 (INTERSECT) and H2020-NMBP-TO-IND-2018-2020 project GA no. 953167 (OpenModel). ICN2 was supported by the CERCA Program of Generalitat de Catalunya, and by grant PGC2018-096955-B-C43 funded by Spain's MCIN/AEI/10.13039/501100011033 and by ERDF "A way of making Europe". L.-S.M. received funding from the European Union's Horizon 2020 research and innovation programme under the Marie Skłodowska-Curie Grant Agreement No. 754558 (PREBIST – COFUND). L.-S.M.'s work has been performed in the context of the Physics Ph.D. program of the Universitat Autònoma de Barcelona. Computer resources were provided in part by the TACC supercomputing center (TX, USA) through QUANTRANS project, and by the Spanish Red Nacional de Supercomputación through Grant Nos. FI-2020-2-0037, FI-2020-3-0034, FI-2021-1-0016, FI-2021-2-0017, FI-2021-3-0031, FI-2022-1-0035, and FI-2022-2-0042 at MareNostrum 4, BSC-CNS.

Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

Keywords

devices, DFT, electrical characteristics, Ginestra, materials, ovonic threshold switching, selectors, trap defects

Received: November 10, 2022

Revised: January 7, 2023

Published online: February 7, 2023

- [1] S. Raoux, W. Wetnic, D. Ielmini, *Chem. Rev.* **2010**, *110*, 240.
- [2] H.-S. P. Wong, S. Raoux, S. Kim, J. Liang, J. P. Reifenberg, B. Rajendran, M. Asheghi, K. E. Goodson, *Proc. IEEE* **2010**, *98*, 2201.
- [3] D. Loke, T. H. Lee, W. J. Wang, L. P. Shi, R. Zhao, Y. C. Yeo, T. C. Chong, S. R. Elliott, *Science* **2012**, *336*, 1566.
- [4] P. Noé, A. Verdy, F. d'Acapito, J.-B. Dory, M. Bernard, G. Navarro, J.-B. Jager, J. Gaudin, J.-Y. Raty, *Sci. Adv.* **2020**, *6*, 1.
- [5] S.-D. Kim, H.-W. Ahn, S. y. Shin, D. S. Jeong, S. H. Son, H. Lee, B.-k. Cheong, D. W. Shin, S. Lee, *ECS Solid State Lett.* **2013**, *2*, Q75.
- [6] J. Song, J. Woo, A. Prakash, D. Lee, H. Hwang, *IEEE Electron Device Lett.* **2015**, *36*, 681.
- [7] S. Kim, J. Zhou, W. D. Lu, *IEEE Trans. Electron Devices* **2014**, *61*, 2820.
- [8] R. E. Simpson, P. Fons, A. v. Kolobov, T. Fukaya, M. Krbal, T. Yagi, J. Tominaga, *Nat. Nanotechnol.* **2011**, *6*, 501.
- [9] O. Abou El Kheir, M. Bernasconi, *Nanomaterials* **2021**, *11*, 2382.
- [10] Y.-H. Lee, P. J. Liao, V. Hou, D. Heh, C.-H. Nien, W.-H. Kuo, G. T. Chen, S.-M. Yu, Y.-S. Chen, J.-Y. Wu, X. Bao, C. H. Diaz, in *2021 IEEE Int. Reliability Physics Symp. (IRPS)*, IEEE, Piscataway **2021**, p. 1.
- [11] P. Noé, C. Vallée, F. Hippert, F. Fillot, J.-Y. Raty, *Semicond. Sci. Technol.* **2018**, *33*, 013002.
- [12] N. Saxena, A. Manivannan, *Phys. Status Solidi RRL* **2022**, *16*, 2200101.
- [13] Z. Chai, W. Zhang, R. Degraeve, S. Clima, F. Hatem, J. F. Zhang, P. Freitas, J. Marsland, A. Fantini, D. Garbin, L. Goux, G. S. Kar, *IEEE Electron. Device Lett.* **2019**, *40*, 1269.
- [14] B. Song, H. Xu, S. Liu, H. Liu, Q. Liu, Q. Li, *Appl. Phys. A* **2019**, *125*, 772.
- [15] C. Yoo, W. Kim, J. W. Jeon, E.-S. Park, M. Ha, Y. K. Lee, C. S. Hwang, *ACS Appl. Mater. Interfaces* **2020**, *12*, 23110.
- [16] G. Liu, T. Li, L. Wu, Y. Chen, B. Liu, Z. Ma, S. Song, Z. Song, *ACS Appl. Nano Mater.* **2019**, *2*, 5373.
- [17] N. S. Avasarala, G. L. Donadio, T. Witters, K. Opsomer, B. Govoreanu, A. Fantini, S. Clima, H. Oh, S. Kundu, W. Devulder, M. H. van der Veen, J. van Houdt, M. Heyns, L. Goux, G. S. Kar, in *2018 IEEE Symp. VLSI Technology*, IEEE, New Jersey **2018**, p. 209.
- [18] H. Li, J. Robertson, *Sci. Rep.* **2019**, *9*, 1867.
- [19] G. Liu, L. Wu, X. Chen, T. Li, Y. Wang, T. Guo, Z. Ma, M. Zhu, S. Song, Z. Song, *J. Alloys Compd.* **2019**, *792*, 510.
- [20] A. Velea, K. Opsomer, W. Devulder, J. Dumortier, J. Fan, C. Detavernier, M. Jurczak, B. Govoreanu, *Sci. Rep.* **2017**, *7*, 8103.
- [21] A. Padovani, B. Kaczer, M. Pesic, A. Belmonte, M. Popovici, L. Nyns, D. Linten, V. v. Afanas'ev, I. Shlyakhov, Y. Lee, H. Park, L. Larcher, *IEEE Trans. Electron. Device* **2019**, *66*, 1892.
- [22] Applied Materials Ginestra, <https://www.appliedmaterials.com/products/applied-mdlx-ginestrasimulation-software>.
- [23] L. Vandelli, A. Padovani, L. Larcher, R. G. Southwick, W. B. Knowlton, G. Bersuker, *IEEE Trans. Electron. Devices* **2011**, *58*, 2878.
- [24] K. Huang, A. Rhys, *Proc. R. Soc. London* **1950**, *204*, 406.
- [25] M. Zhang, Z. Huo, Z. Yu, J. Liu, M. Liu, *J. Appl. Phys.* **2011**, *110*, 114108.
- [26] D. I. Bletskan, J. J. Madyar, V. N. Kabaciy, *Semiconductors* **2006**, *40*, 137.
- [27] Z. Jahangirli, *Turk. J. Phys.* **2011**.
- [28] S. Plimpton, *J. Comput. Phys.* **1995**, *117*, 1.
- [29] J. M. Soler, E. Artacho, J. D. Gale, A. García, J. Junquera, P. Ordejón, D. Sánchez-Portal, *J. Phys.: Condens. Matter* **2002**, *14*, 2745.
- [30] A. García, N. Papior, A. Akhtar, E. Artacho, V. Blum, E. Bosoni, P. Brandimarte, M. Brandbyge, J. I. Cerdá, F. Corsetti, R. Cuadrado, V. Dikan, J. Ferrer, J. Gale, P. García-Fernández, V. M. García-Suárez, S. García, G. Huhs, S. Illera, R. Korytár, P. Koval, I. Lebedeva, L. Lin, P. López-Tarifa, S. G. Mayo, S. Mohr, P. Ordejón, A. Postnikov, Y. Pouillon, M. Pruneda, et al., *J. Chem. Phys.* **2020**, *152*, 204108.
- [31] P. Giannozzi, O. Barone, P. Bonfà, D. Brunato, R. Car, I. Carnimeo, C. Cavazzoni, S. de Gironcoli, P. Delugas, F. Ferrari Ruffino, A. Ferretti, N. Marzari, I. Timrov, A. Urru, S. Baroni, *J. Chem. Phys.* **2020**, *152*, 154105.
- [32] F. Tavanti, B. Dianat, A. Catellani, A. Calzolari, *ACS Appl. Electron. Mater.* **2020**, *2*, 2961.
- [33] F. Tavanti, A. Calzolari, *ACS Omega* **2022**, *7*, 23255.
- [34] E. Artacho, D. Sanchez-Portal, P. Ordejón, A. Garcia, J. M. Soler, *Phys. Status Solidi B* **1999**, *215*, 809.
- [35] S. Jia, N. Shi, J. Shen, R. Wu, Q. Liu, Z. Song, M. Zhu, *Phys. Status Solidi B* **2021**, *258*, 2100038.
- [36] M. Cobb, D. A. Drabold, R. L. Cappelletti, *Phys. Rev. B* **1996**, *54*, 12162.
- [37] J. Y. Raty, V. v. Godlevsky, J. P. Gaspard, C. Bichara, M. Bionducci, R. Bellissent, R. Céolin, J. R. Chelikowsky, Ph. Ghosez, *Phys. Rev. B* **2001**, *64*, 235209.
- [38] S. Clima, D. Garbin, K. Opsomer, N. S. Avasarala, W. Devulder, I. Shlyakhov, J. Keukelier, G. L. Donadio, T. Witters, S. Kundu, B. Govoreanu, L. Goux, C. Detavernier, V. Afanas'ev, G. S. Kar, G. Pourtois, *Phys. Status Solidi RRL* **2020**, *14*, 1900672.
- [39] N. C. Murphy, R. Wortis, W. A. Atkinson, *Phys. Rev. B* **2011**, *83*, 184206.
- [40] S. Caravati, M. Bernasconi, T. D. Kühne, M. Krack, M. Parrinello, *J. Phys.: Condens. Matter* **2010**, *22*, 399801.
- [41] W. Zhou, M. Paesler, D. E. Sayers, *Phys. Rev. B* **1991**, *43*, 2315.
- [42] E. Gulbrandsen, H. B. Johnsen, M. Endregaard, T. Grande, S. Stølen, *J. Solid State Chem.* **1999**, *145*, 253.
- [43] M. T. M. Shatnawi, C. L. Farrow, P. Chen, P. Boolchand, A. Sartbaeva, M. F. Thorpe, S. J. L. Billinge, *Phys. Rev. B* **2008**, *77*, 094134.
- [44] M. Xu, M. Xu, X. Miao, *InfoMat* **2022**, *4*.
- [45] A. Padovani, L. Larcher, G. Bersuker, P. Pavan, *IEEE Electron. Device Lett.* **2013**, *34*, 680.
- [46] A. Padovani, D. Z. Gao, A. L. Shluger, L. Larcher, *J. Appl. Phys.* **2017**, *121*, 155101.
- [47] L. Larcher, F. M. Puglisi, A. Padovani, L. Vandelli, P. Pavan, in *2016 26th Int. Workshop on Power and Timing Modeling, Optimization and Simulation (PATMOS)*, IEEE, USA **2016**, p. 128.
- [48] M. Pesiu, V. di Lecce, D. Pramanik, L. Larcher, in *2018 Int. Conf. Simulation of Semiconductor Processes and Devices (SISPAD)*, IEEE, USA **2018**, p. 111.
- [49] K. A. Nasyrov, V. A. Gritsenko, *J. Appl. Phys.* **2011**, *109*, 093705.
- [50] G. Jegert, A. Kersch, W. Weinreich, P. Lugli, *IEEE Trans. Electron. Devices* **2011**, *58*, 327.

- [51] P. Vashishta, R. Kalia, I. Ebbsjo, *Solid State Ionics* **1989**, 32, 872.
- [52] M. J. van Setten, M. Giantomassi, E. Bousquet, M. J. Verstraete, D. R. Hamann, X. Gonze, G.-M. Rignanese, *Comput. Phys. Commun.* **2018**, 226, 39.
- [53] J. P. Perdew, K. Burke, M. Ernzerhof, *Phys. Rev. Lett.* **1996**, 77, 3865.
- [54] G. Román-Pérez, J. M. Soler, *Phys. Rev. Lett.* **2009**, 103, 096102.
- [55] T. Thonhauser, V. R. Cooper, S. Li, A. Puzder, P. Hyldgaard, D. C. Langreth, *Phys. Rev. B* **2007**, 76, 125112.
- [56] A. Bouzid, C. Massobrio, M. Boero, G. Ori, K. Sykina, E. Furet, *Phys. Rev. B* **2015**, 92, 134208.
- [57] L. A. Agapito, S. Curtarolo, M. B. Nardelli, *Phys. Rev. X* **2015**, 5, 011006.
- [58] T. D. Kühne, M. Iannuzzi, M. del Ben, V. v. Rybkin, P. Seewald, F. Stein, T. Laino, R. Z. Khaliullin, O. Schütt, F. Schiffmann, D. Golze, J. Wilhelm, S. Chulkov, M. H. Bani-Hashemian, V. Weber, U. Borštnik, M. Taillefumier, A. S. Jakobovits, A. Lazzaro, H. Pabst, T. Müller, R. Schade, M. Guidon, S. Andermatt, N. Holmberg, G. K. Schenter, A. Hehn, A. Bussy, F. Belleflamme, G. Tabacchi, et al., *J. Chem. Phys.* **2020**, 152, 194103.
- [59] J. Heyd, G. E. Scuseria, M. Ernzerhof, *J. Chem. Phys.* **2003**, 118, 8207.