

# A Scalable Quadratic Nonlinear Silicon Photonics Platform With Printable Entangled Photon-Pair Sources

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The integration of second-order optical nonlinearities into scalable photonic platforms remains a key challenge due to their large sensitivity to fabrication variations. Here, a scalable quadratic nonlinear platform is presented that harnesses the maturity and scalability of existing CMOS processes by heterogeneously integrating periodically poled lithium niobate (PPLN) onto a silicon photonics platform. A generic PPLN design enables frequency conversion on two distinct waveguide geometries with efficiencies comparable to reported lithium niobate on insulator (LNOI) rib waveguides. Reproducible phase-matching is achieved across the full radius of a commercial 200 mm silicon photonics wafer, leveraging superior CMOS fabrication tolerances. Furthermore, a tuning mechanism is introduced for both blue- and red-shifting of the operating wavelength, fully compensating fabrication-induced offsets. This enables deterministic phase-matching over an entire wafer and yields a strategy for wafer-scale phase-matched quadratic nonlinearities. Finally, printable photon-pair sources are realized via spontaneous parametric down-conversion (SPDC), highlighting the platform's potential for large-scale quantum optical circuits. These results pave the way for wafer-scale integration of second-order optical nonlinearities in large photonic systems.

efficiencies more than 20 times higher than those of previous in-diffused waveguides.<sup>[3–5]</sup> Moreover, new nonlinear regimes are unlocked due to the strong geometric dispersion associated with the high index contrast. By leveraging the ability to periodically pole the lithium niobate, quasi-phase-matching can be achieved while simultaneously engineering higher-order dispersion through waveguide design. This advancement has led to several demonstrations, including ultrabroadband frequency conversion,<sup>[6,7]</sup> large-gain optical parametric amplification<sup>[8]</sup> and spectrally-separable photon-pair generation.<sup>[9]</sup> The nonlinear response can be further enhanced by incorporating the PPLN into a resonator, yielding normalized conversion efficiencies up to 5 000 000 %/W<sup>[10]</sup> and low-threshold optical parametric oscillators.<sup>[11,12]</sup>

Unfortunately, the same geometric dispersion leads to significant fabrication sensitivity of the phase-matching condition.<sup>[13,14]</sup> Non-uniformity along the waveguide limits the achieved conversion efficiencies while fabrication deviations from the intended geometry cause a substantial shift in the operating wavelength, preventing deterministic phase-matching. Solutions have been proposed based on in-depth characterization of the waveguide geometry through advanced metrology, followed by locally adapting the poling period<sup>[15,16]</sup> or waveguide dimen-

## 1. Introduction

Over the past decade, the efficiency of optical frequency conversion processes has significantly improved, driven by the emergence of the LNOI platform.<sup>[1,2]</sup> The introduction of etched LN waveguides with quadratic nonlinearities has enabled sub-wavelength mode confinement, leading to nonlinear conversion

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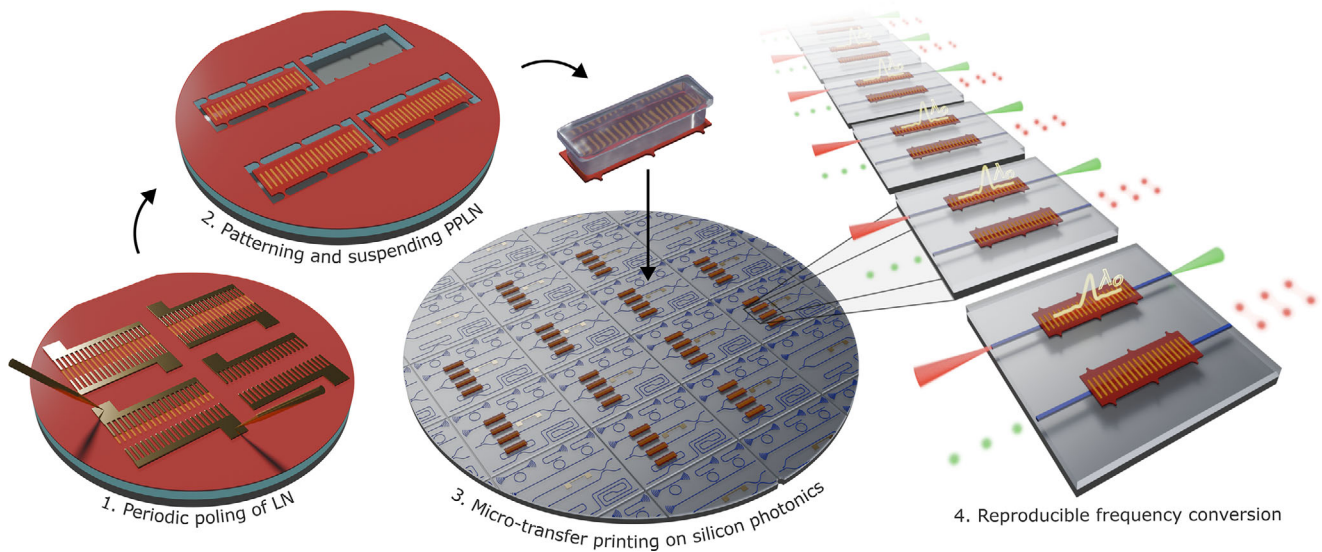
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**Figure 1.** A scalable quadratic nonlinear silicon photonics platform. 1 Thin-film lithium niobate is periodically poled through electric field poling. 2 PPLN films are patterned and suspended. 3 PPLN films are micro-transfer printed on exposed silicon nitride waveguides. 4 Reproducible frequency conversion and photon-pair generation are obtained.

sions<sup>[17]</sup> in subsequent fabrication steps. However, this severely limits the scalability to larger volumes, as every wafer requires a custom lithography mask, which is time-consuming and expensive.

In contrast, the silicon photonics platform offers much better fabrication tolerances and is inherently scalable, benefiting from well-established CMOS manufacturing techniques.<sup>[18]</sup> Especially, the silicon nitride platform has gained popularity for nonlinear and quantum applications due to its large transparency window, low losses and power-handling capabilities.<sup>[19,20]</sup> Nevertheless, both silicon and silicon nitride waveguides lack a  $\chi^{(2)}$  component due to their inversion symmetry, inhibiting efficient frequency conversion. Quadratic nonlinearities can be induced through symmetry-breaking methods,<sup>[21,22]</sup> but the conversion efficiencies are typically much weaker than the LNOI platform.

Heterogeneous integration of lithium niobate on a silicon photonics platform is able to combine the high nonlinear efficiencies of the LNOI platform with the fabrication tolerances and scalability of silicon photonics. While several demonstrations have successfully integrated thin-film lithium niobate,<sup>[23–26]</sup> effective use of the  $\chi^{(2)}$  component through a phase-matched process remains elusive.

In this work, we establish a nonlinear silicon photonics platform by micro-transfer printing PPLN onto silicon photonics (see **Figure 1**). This method enables back-end integration of thin-film lithium niobate, preventing lithium contamination<sup>[27]</sup> during CMOS processing while preserving the scalability of the silicon photonics platform.<sup>[28]</sup> We demonstrate quasi-phase-matched frequency conversion and investigate reproducible and deterministic phase-matching, leading to a strategy for scalable quadratic nonlinearities. Additionally, we establish printable entangled photon-pair sources by characterizing spontaneous parametric down-conversion in the proposed platform.

Our approach paves the way for scalable quadratic nonlinearities in large-scale nonlinear photonic circuits, from arrays of in-

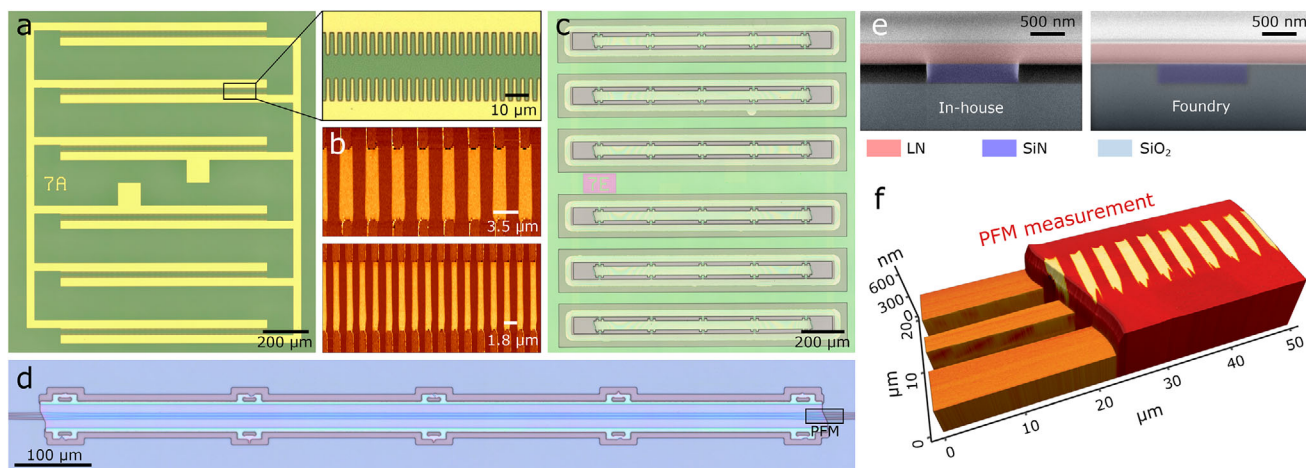
terfering parametric photon-pair sources to the scalable fabrication of low-threshold optical parametric oscillators.

## 2. Results

### 2.1. Fabrication of the Nonlinear Waveguide

The process flow of our nonlinear silicon photonics platform begins with the periodic poling of X-cut thin-film lithium niobate. Starting from an LNOI wafer (300 nm LN thickness), electrodes are deposited for electric field poling with a period matching the designed phase-matching process (**Figure 2a**). Similar to previous works,<sup>[29,30]</sup> a custom electric waveform is applied to control domain growth vertically while keeping horizontal growth limited. This ensures the formation of straight domains across various poling periods (3.5  $\mu\text{m}$  and 1.8  $\mu\text{m}$  shown in **Figure 2b**), which are characterized through piezoresponse force microscopy (PFM). Due to the pick-and-place approach, positioning of the poling electrodes is independent of the final photonic circuit layout. This allows us to arrange our nonlinear devices in a dense array or matrix configuration, where the electrodes are connected in parallel without crossing any future waveguides. A single waveform then poles a full set of devices simultaneously, making it possible to efficiently scale the poling process to larger volumes.

After poling the lithium niobate, the PPLN is patterned into rectangular films of 40  $\mu\text{m} \times 1 \text{mm}$  and suspended by etching the buried oxide layer with hydrofluoric acid (**Figure 2c**). Only a few anchoring points keep the devices connected to the substrate. A polymer PDMS stamp is then used to pick up the films with a commercial micro-transfer printing tool, breaking these connections and fully detaching them. Next, they are printed onto exposed 300 nm thick silicon nitride waveguides, forming a hybrid PPLN/SiN geometry (**Figure 2d**). An in-depth description of the transfer printing flow is given by Ref. [31]. Two silicon ni-



**Figure 2.** Platform process flow. a) Optical microscope image of deposited electrodes for electric field poling of 6 LN films. b) PFM images visualizing the straight poled domains for a 3.5  $\mu\text{m}$  (upper) and 1.8  $\mu\text{m}$  (lower) poling period. c) Optical microscope image of suspended PPLN thin films, ready to be picked. d) Optical microscope image of micro-transfer printed PPLN film onto a silicon nitride waveguide, fabricated in-house. e) False-color SEM images of PPLN/SiN waveguide cross sections on the in-house (left) and foundry (right) platform (with Pt deposited on top for a cleaner ion-beam cut). f) Visualization of an AFM measurement with a PFM color overlay, measured at the transition from a silicon nitride waveguide (indicated on Figure 2d). This demonstrates that the poled domains are positioned above the nitride waveguide.

tride platforms are used: an in-house prototyping platform based on electron beam lithography and a 200 mm silicon photonics foundry platform.<sup>[32]</sup> Cross sections of the fabricated hybrid waveguides are shown in Figure 2e, where the in-house platform features 3  $\mu\text{m}$ -wide air trenches, while the foundry platform is fully planarized. A simultaneous atomic force microscopy (AFM) and PFM measurement at the transition to the hybrid waveguide confirms that the poled domains are aligned above the silicon nitride waveguide (Figure 2f). The fabricated devices incorporate angled waveguide transitions at 25° to avoid parasitic reflections, which dominate the insertion losses of the component (see Figure S2, Supporting Information). The outcome is a quasi-phase-matched nonlinear waveguide on a silicon photonics platform.

## 2.2. Efficient Frequency Conversion on Distinct Silicon Photonics Platforms

Our nonlinear silicon photonics platform decouples the poling process from the photonic circuit design, offering increased flexibility. By adapting the nitride waveguide width, the same PPLN film can be used to obtain phase-matching at a given wavelength for different layer stacks (see Figure S6, Supporting Information). This approach enhances scalability, as an entire wafer of identical PPLN films can be leveraged to accommodate phase-matching requirements across multiple silicon photonics platforms, each operating at several wavelengths.

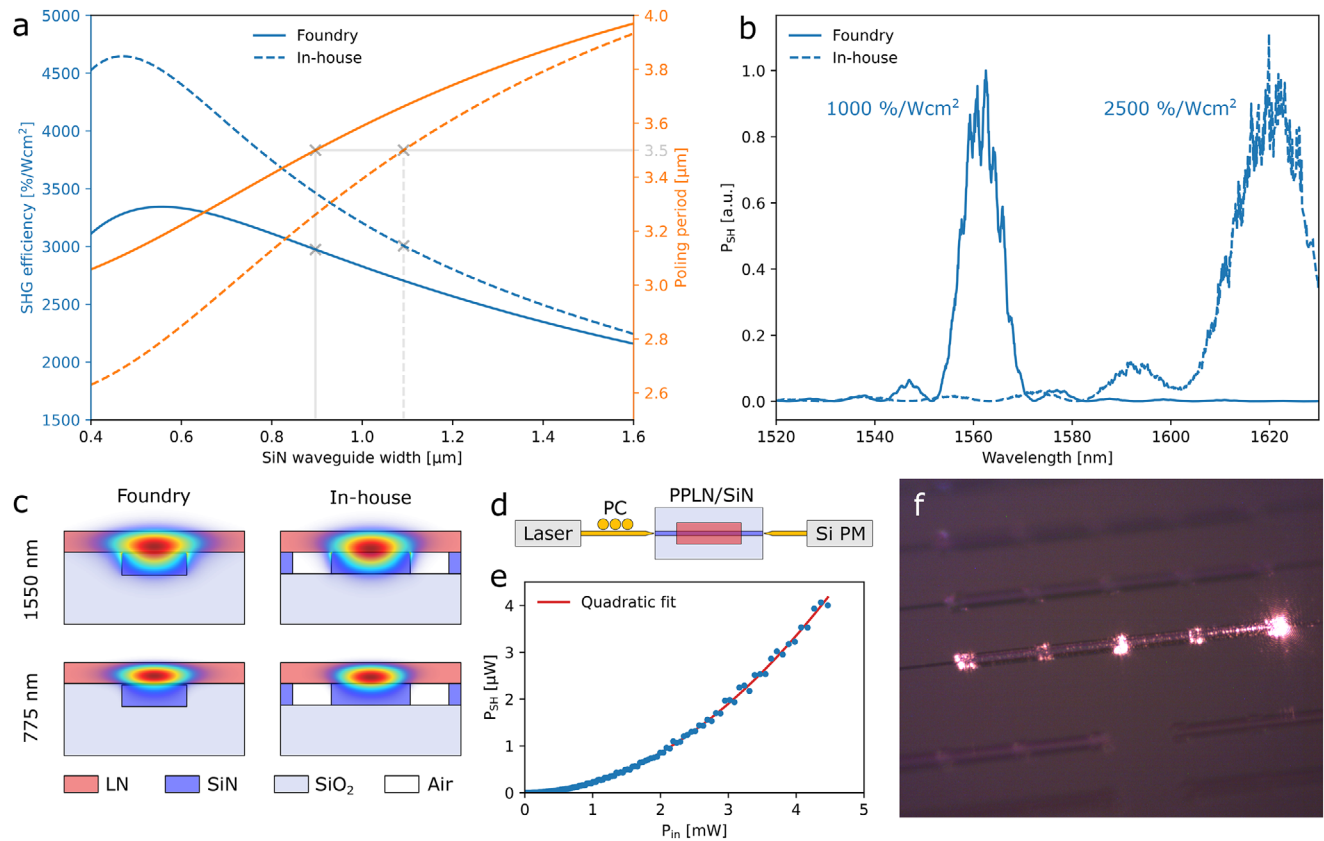
We demonstrate this versatility by employing the same generic PPLN design for our in-house and foundry platform. A LN thickness of 300 nm and poling period of 3.5  $\mu\text{m}$  are taken to enable phase-matching on both platforms for waveguide widths around 900 nm and 1100 nm, respectively (Figure 3a). These amount to simulated nonlinear efficiencies of about 3000 %/Wcm<sup>2</sup> for the type-0 second harmonic generation (SHG) process at 1550 nm.

The involved TE<sub>00</sub> modes are shown in Figure 3c. After fabrication, we achieve quasi-phase-matched SHG on both platforms, at 1620 nm (in-house) and at 1560 nm (foundry) (Figure 3b). A deviation in the LN thickness from the intended 300 nm causes a notable shift from the 1550 nm target wavelength of the former, highlighting the need for a strategy to achieve deterministic phase-matching, as discussed further. Nevertheless, quasi-phase-matching is observed with conversion efficiencies of 2500 %/Wcm<sup>2</sup> (in-house) and 1000 %/Wcm<sup>2</sup> (foundry), comparable to reported LNOI rib waveguides.<sup>[3]</sup> 3-dB bandwidths of 15 and 8 nm are measured, respectively, related to the higher-order dispersion of the waveguide geometries. Furthermore, a measured quadratic power dependence, along with direct imaging of the second harmonic build-up in the PPLN/SiN waveguide, confirms the SHG process (Figure 3e,f). These results demonstrate the successful heterogeneous integration of quasi-phase-matched quadratic nonlinearities on distinct silicon photonics platforms.

## 2.3. Chip-to-Chip Reproducibility on a 200 mm Silicon Photonics Wafer

Current LNOI rib waveguides struggle with phase-matching reproducibility due to their high sensitivity to fabrication variations, which stem from two main sources. First, the chemical mechanical polishing during LNOI manufacturing leads to typical LN film thickness variations of 10–20 nm across the wafer.<sup>[16]</sup> Second, the subsequent shallow etch used to define the rib waveguides suffers from a challenging process control, resulting in additional etch depth variations and non-uniformity on the order of 20 nm.<sup>[16]</sup> Consequently, large deviations are observed from the intended phase-matching wavelength.

Our hybrid approach seeks to improve reproducibility by relying on silicon nitride strip waveguides, which benefit from



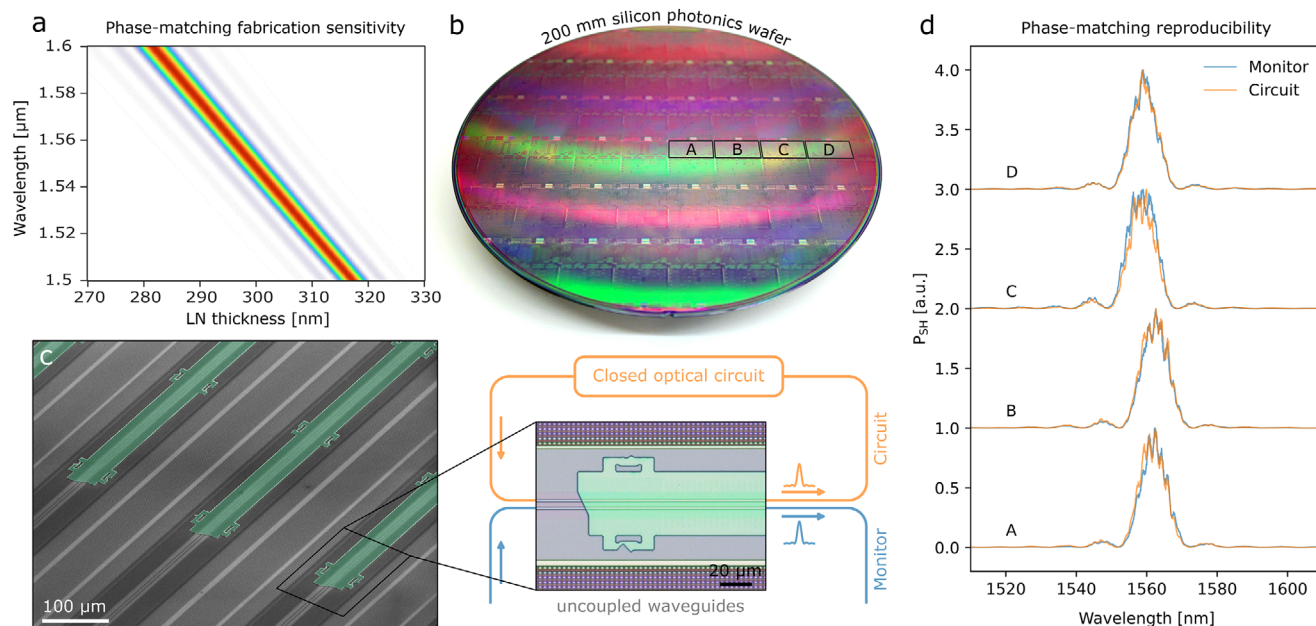
**Figure 3.** Quasi-phase-matched frequency conversion. a) Numerical simulation of the SHG efficiency and corresponding poling period as function of the silicon nitride width, for a pump at 1550 nm. The chosen 3.5 μm period is indicated, giving SiN widths of 900 nm (foundry) and 1100 nm (in-house). b) Measured SHG frequency conversion on foundry and in-house platform. c) Mode profiles of the fundamental TE<sub>00</sub> and second harmonic TE<sub>00</sub> in the hybrid waveguides. d) Sketch of the fiber-based measurement setup with polarization controller (PC), silicon power meter (PM) and lensed fibers. e) Measured quadratic power dependence of the frequency conversion. f) Photograph of the generated second harmonic in a PPLN/SiN waveguide.

superior fabrication tolerances due to their mature wafer-scale CMOS processes on 200 and 300 mm wafers.<sup>[33,34]</sup> This eliminates variations associated to the shallow etch, leaving only original LN thickness deviations as dominant factor. Additionally, as the micro-transfer printing method enables dense packing of PPLN films on the LNOI source wafer (Figure 2c), neighboring films from the same local area can be used to populate the silicon photonics wafer. This minimizes differences in thickness between devices and further improves reproducibility over the wafer.

Generally, a discrepancy from the intended film thickness will remain, introducing a consistent offset in phase-matching over the full wafer. For every nanometer difference in thickness, a shift of 2.8 nm (5.0 nm) in phase-matching wavelength is expected for the foundry (in-house) nitride platform (Figure 4a; Figure S7, Supporting Information), signaling the need for a tuning mechanism to achieve deterministic phase-matching, as discussed further. Direct access to the nonlinear waveguide is then typically desired for optical characterization of the offset, which imposes constraints on the design of more complex nonlinear circuits. We circumvent this issue by adopting a dual-waveguide approach (Figure 4c). Two parallel but uncoupled waveguides are covered with the same PPLN film, ensuring they have identical domain structures and waveguide geometry along their entire length.

As a result, both waveguides are expected to exhibit the same phase-matching behavior, enabling one to be integrated into a closed optical circuit while the other functions as a dedicated monitor.

To assess the reproducibility of our platform, we fabricate nonlinear devices over the full radius of a 200 mm silicon photonics wafer, designed for second harmonic generation at 1550 nm. PPLN films are picked from a single LNOI source and printed onto the four chips that constitute the wafer radius, as indicated in Figure 4b. The corresponding optical characterization for each chip is shown in Figure 4d. A perfect overlap is observed between the phase-matching curves of each monitor and circuit waveguide. This validates the dual-waveguide approach, where a monitor can reliably probe the phase-matching in a closed nonlinear circuit. Comparing the different chips across the wafer radius reveals nearly-identical phase-matching for the same waveguide designs. The minor remaining shifts can be fully attributed to the original non-uniformity of the LNOI source, which would amount to 1–2 nm according to simulations. Furthermore, the micro-transfer printing method introduces no detectable variations between different printing steps. Although tested along a single radius, the measured reproducibility strongly suggests that nearly-identical phase-matching can be achieved over the entire 200 mm wafer.



**Figure 4.** Reproducible phase-matching on a 200 mm silicon photonics wafer. a) Simulated SHG phase-matching sensitivity to the LN thickness. b) Photograph of the 200 mm silicon photonics foundry wafer. c) False-color SEM image of printed PPLN films on the foundry platform, with an optical microscope image of the dual-waveguide design relying on two uncoupled (circuit and monitor) waveguides sharing the same PPLN film. d) Measured phase-matching reproducibility with identical phase-matching between monitor and circuit waveguides, and nearly-identical phase-matching over the full 200 mm wafer radius (indicated on Figure 4b).

#### 2.4. Tunability for Wafer-Scale Deterministic Phase-Matching

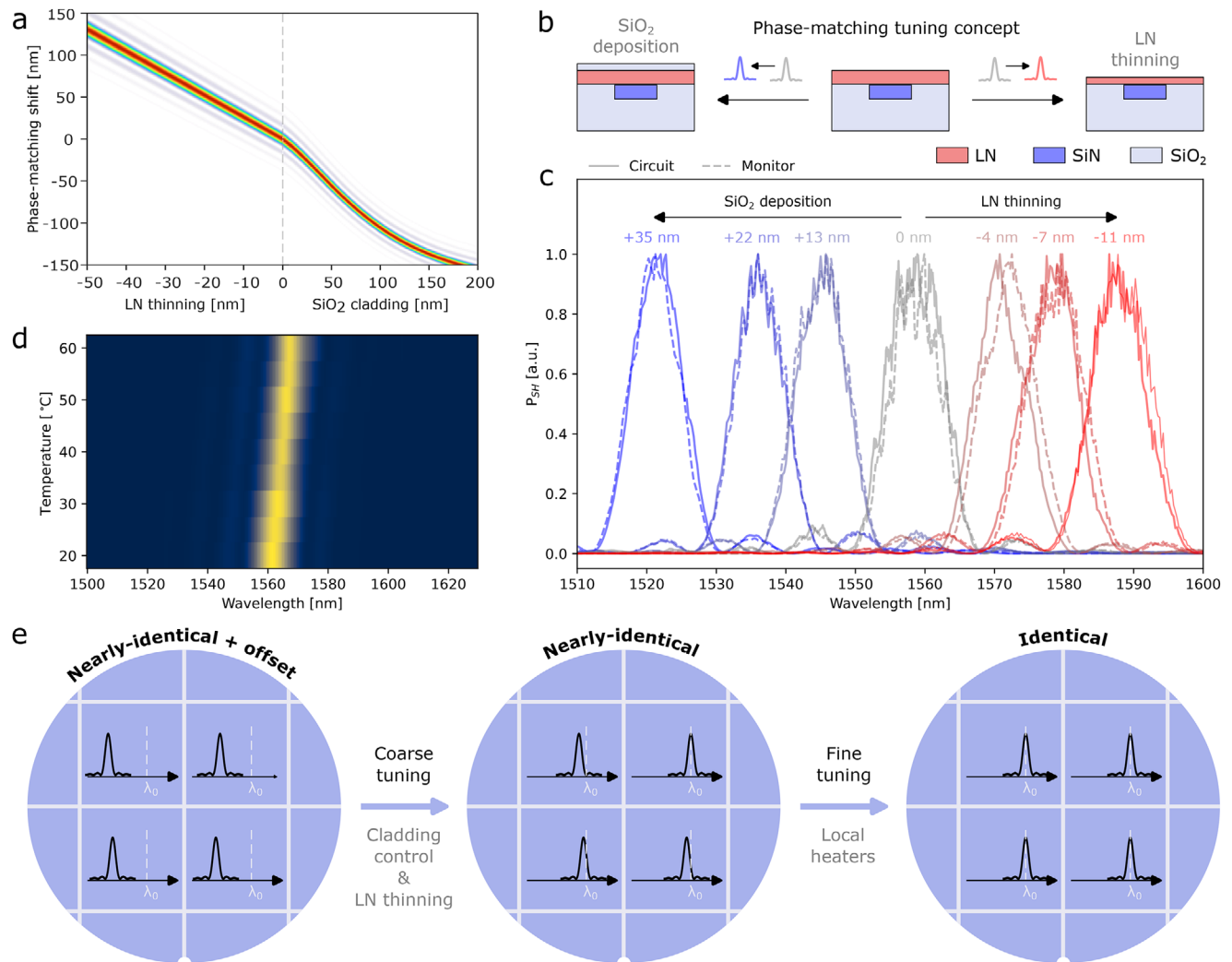
While consistent across the wafer, there remains an offset relative to the designed wavelength, indicating the need for a tuning mechanism to reach deterministic phase-matching. Thermo-optic tuning is a common approach to shift the phase-matching, and characterization of our platform reveals a tuning efficiency of 0.18 nm/K (Figure 5d). Unfortunately, fully neutralizing offsets that can exceed 50 nm would require impractically high temperatures, making this method unfeasible by itself. An alternative tuning approach is therefore necessary.

We propose a single-step process for simultaneous phase-matching tuning over a full wafer. Although the high fabrication sensitivity introduces a consistent offset, it also implies that small controlled modifications to the waveguide geometry can create significant wavelength shifts. The hybrid waveguide geometry is particularly well-suited for this, as it features a single accessible flat interface while the nitride waveguide remains protected. Effectively altering the LN thickness then allows for significant phase-matching shifts (Figure 5b). The nonlinear process can be blue-shifted through controlled cladding deposition (e.g. SiO<sub>2</sub>) on the hybrid waveguide. This pulls the optical modes upwards, effectively mimicking an increase in LN film thickness. Conversely, red-shifting is achieved by thinning the printed PPLN, which pushes the modes downwards. To accomplish this, we use a modified RCA-1 clean (NH<sub>4</sub>OH:H<sub>2</sub>O<sub>2</sub> 1:4), a method known to etch lithium niobate while maintaining a good surface roughness<sup>[35,36]</sup> (measured RMS roughness = 0.33 nm, supplementary Figure S8). Since only the +x crystal plane is exposed in our waveguide geometry, poled and unpoled domains etch uniformly, unaffected by etch anisotropy. Additionally, we measure

a high etching selectivity of 30 with silicon oxide (compared to a typical selectivity of 1 for Ar-based physical etches), allowing for precise thinning of the nonlinear component without affecting the silicon photonics circuit. These post-printing modifications enable phase-matching shifts of over 100 nm in both directions (Figure 5a), fully correcting for fabrication-induced offsets and making deterministic phase-matching possible.

We validate this tuning concept by characterizing the phase-matching curves for different cladding thicknesses and etch depths (see Figure 5c). Starting from an operating wavelength of 1560 nm, we test oxide thicknesses on the order of a few tens of nanometers, achieving phase-matching across the entire C-band, down to 1520 nm. Conversely, thinning the LN by just a few nanometers significantly shifts the phase-matching to higher wavelengths, as demonstrated up to 1590 nm. In both cases, the circuit and monitor experience identical tuning, preserving their overlap and therefore ensuring reliable monitoring of a closed nonlinear circuit.

Based on these results, we propose a strategy for a wafer-scale quadratic nonlinear platform (Figure 5e). Through micro-transfer printing, a full silicon photonics wafer can be populated with PPLN films sourced from the same region of the LNOI wafer. Combined with the mature CMOS fabrication tolerances, this approach minimizes device non-uniformity and leads to nearly-identical phase-matching with a consistent offset from the target wavelength across the full wafer. Optical characterization of the nonlinear circuits through a monitor determines the remaining shift. Coarse tuning by cladding control (blue-shifting) or LN thinning (red-shifting) across the full wafer simultaneously aligns the phase-matching to the desired working point. By staying slightly blue-shifted, local heaters can individually fine-tune



**Figure 5.** Phase-matching tunability for wafer-scale nonlinearities. a) Simulated phase-matching tuning through SiO<sub>2</sub> cladding or LN thinning. b) Phase-matching tuning concept through waveguide modification. c) Measured simultaneous tuning of circuit and monitor waveguide through cladding control (blue-shift) and LN thinning (red-shift). d) Measured temperature dependence of the phase-matching with a 0.18 nm/K efficiency. e) Strategy to obtain identical phase-matching over a full silicon photonics wafer through coarse (cladding control & LN thinning) and fine (local heaters) tuning.

the remaining measured few-nanometer variations between devices. Ultimately, this approach yields an entire silicon photonics wafer with uniformly and deterministically phase-matched quadratic nonlinearities at the designed operating wavelength.

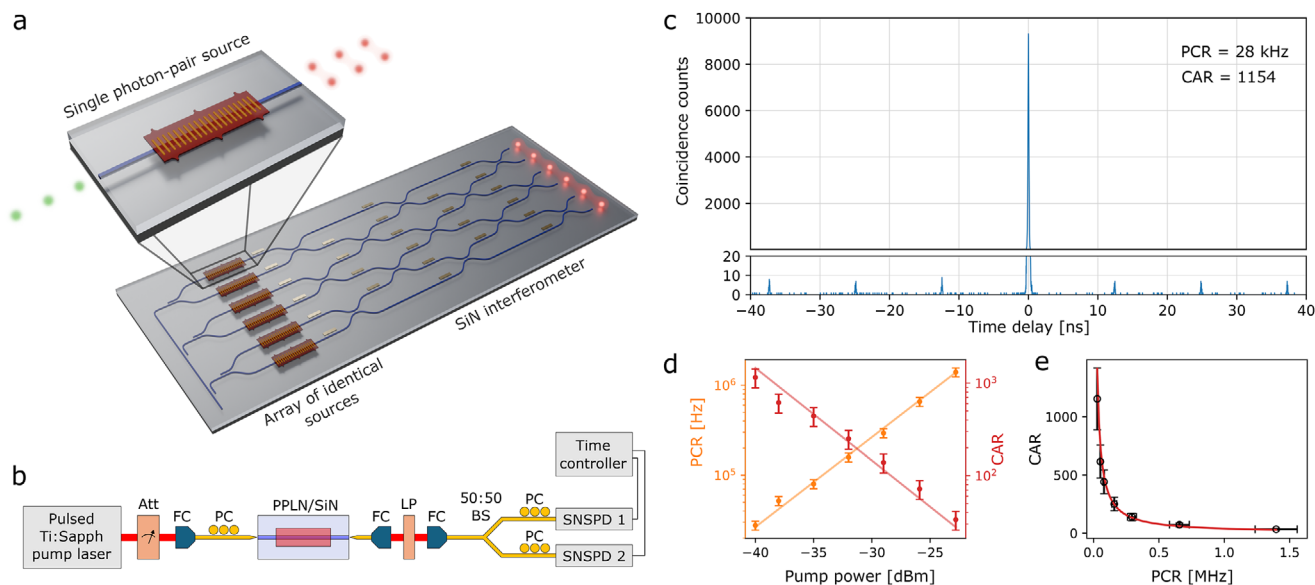
## 2.5. Printable Entangled Photon-Pair Sources

Parametric optical nonlinearities are widely employed for quantum state generation,<sup>[37–39]</sup> including squeezed states<sup>[40]</sup> and heralded single-photon sources.<sup>[41]</sup> However, the technological challenges involved with reproducible phase-matching of quadratic nonlinearities have hindered their use in large-scale integrated photonic circuits, with demonstrations limited to few interfering devices.<sup>[42]</sup> Large-system implementations typically fall back on four-wave mixing (FWM) approaches with relaxed phase-matching requirements, often benefiting from the scalability of the silicon photonics platform.<sup>[43,44]</sup> Even so, second-order non-

linearities provide distinct advantages over FWM. Their higher efficiency greatly reduces the required pump power, thereby mitigating parasitic nonlinear effects such as Raman noise. Furthermore, the large spectral separation between the pump and generated photon state significantly simplifies filter design for broadband and high-extinction pump suppression.

Our nonlinear platform represents a major step toward integrating quadratic nonlinearities into large-scale photonic quantum systems. For instance, arrays of efficient photon-pair sources with identical phase-matching curves can be printed in front of large reconfigurable interferometers to generate complex entangled states (Figure 6a). For that purpose, we characterize spontaneous parametric down-conversion in our nonlinear waveguides and establish printable photon-pair sources.

A Hanbury–Brown–Twiss setup is used to measure the second-order correlation function  $g^{(2)}$ , as shown in Figure 6b. A tunable pulsed Ti:Sapph laser at the second harmonic wavelength with a repetition rate of 80 MHz serves as pump and is coupled



**Figure 6.** Printable entangled photon-pair sources. a) Conceptual sketch of a large photonic quantum system with an array of identical printed photon-pair sources in front of a large SiN interferometer. b) Hanbury–Brown–Twiss measurement setup with variable attenuator, fiber collimators (FC), lensed fibers, longpass filter (LP), beam splitter (BS), polarization controller (PC) and superconducting nanowire single-photon detectors (SNSPD), which are connected to a time controller. c) Measured  $g^{(2)}$  correlation histogram for a coincidence-to-accidental ratio (CAR) of 1154. d) On-chip pair coincidence rate (PCR) and measured CAR as function of on-chip average pump power. e) Measured CAR as function of on-chip PCR.

to the nonlinear waveguide through a lensed fiber. The generated photon pairs are coupled out and sent through a long-pass filter for pump suppression. The photons are then split with a 50:50 beam splitter and sent to superconducting nanowire single-photon detectors (SNSPD), which are connected to a time controller.

We measure the correlation function and investigate the coincidence-to-accidental ratio (CAR) and on-chip pair coincidence rate (PCR) as function of the pump power. The correlation histogram corresponding to a CAR of 1154 and an on-chip PCR of 28 kHz (with an on-chip average pump power of  $-40.0$  dBm) is shown in Figure 6c. Clear bunching is observed with repeating peaks at 12.5 ns spacing, matching the pump repetition rate. Adjusting the pump power through a variable attenuator reveals a linear dependence of the PCR as function of pump power (Figure 6d), with  $\text{CAR} \sim \text{PCR}^{-1}$  (Figure 6e), indicative to the SPDC process. These results confirm efficient photon-pair generation, establishing printable quadratic photon-pair sources on the scalable silicon photonics platform.

### 3. Discussion

As previously noted, micro-transfer printing allows for efficient material use due to the high density of devices on the LNOI source wafer. This leads to reduced thickness variations between devices and therefore improved reproducibility, where remaining shifts can be compensated through local heaters. We can verify the viability of this approach by estimating the temperature needed for full compensation.

For chip sizes of  $1 \times 1$  cm, a 200 mm silicon photonics wafer can host approximately 270 chips. Keeping in mind that the PPLN devices would typically be only a small part of a larger photonic circuit, 10 devices per chip (i.e., per  $\text{cm}^2$ ) would be a re-

alistic estimate. Our source design can further be optimized to a density of 7 devices/ $\text{mm}^2$ , leading to an area of  $4 \text{ cm}^2$  (equivalent to  $2 \times 2$  cm) on the LNOI source to populate an entire 200 mm silicon photonics wafer. Said differently, a single 4 inch LNOI wafer is able to populate over ten 200 mm wafers. The phase-matching deviations over an entire silicon photonics wafer will then be dictated by the non-uniformity of a  $2 \times 2$  cm LNOI region rather than the full wafer. A variation of 5 nm over the local region could then be completely compensated through a maximum temperature difference of 75 K (based on the sensitivity of Figure 4a and tuning rate of Figure 5d), which is well within the capacity of standard on-chip heaters.<sup>[45,46]</sup> Improved LNOI manufacturing or pre-selection of uniform areas can reduce the required temperature tuning even further.

Micro-transfer printing offers several key advantages. As a back end of line (BEOL) integration technique, it leaves the CMOS processing of the silicon photonics wafer unaffected. The cladding can be locally etched to access the nitride waveguide, onto which the PPLN is printed. Therefore, the existing process design kits can be leveraged and coarse tuning of the phase-matching does not alter the silicon photonics circuit. Moreover, the integration can be scaled to larger volumes by accommodating an array of posts on the PDMS stamp and transferring many devices in parallel.<sup>[47]</sup> Additionally, smart design of the exposed waveguide allows phase-matching across a range of wavelengths through a single generic PPLN design (see Figure S6). This approach requires only one LNOI lithography mask set to support different circuit designs, making it both cost-effective and scalable. Furthermore, the current PPLN design can be improved by adding broadband mode couplers to reduce insertion losses, as has been demonstrated by previous works.<sup>[24,26]</sup>

In this work, we have demonstrated precise control and reproducibility of phase-matching, focusing on the SHG and SPDC

processes. This approach is also applicable to other second-order processes, going from sum frequency generation to optical squeezing, and more complex circuits can be used, e.g. by incorporating PPLN into a resonator to achieve printable optical parametric oscillators. The micro-transfer printing method further enables co-integration with other components, such as mode-locked lasers,<sup>[48]</sup> high-speed modulators<sup>[23]</sup> and ultra-fast photodiodes,<sup>[49]</sup> to construct complete single-chip photonic systems.

#### 4. Conclusion

Periodically poled LN rib waveguides offer highly efficient optical frequency conversion but suffer from extreme sensitivity to fabrication variations, limiting their scalability to larger systems. We present a scalable quadratic nonlinear platform that harnesses the maturity of existing CMOS processing by heterogeneously integrating periodically poled LN onto silicon photonics. A generic PPLN design enables phase-matching on two distinct platforms with nonlinear efficiencies comparable to reported LNOI rib waveguides. A dual-waveguide approach is introduced to allow monitoring in closed optical nonlinear circuits and reproducibility is shown over a complete 200 mm silicon photonics wafer radius. A tuning mechanism based on controlled waveguide modifications over the full wafer simultaneously leads to blue- and red-shifting, fully compensating any fabrication-induced offsets. Based on these results, a wafer-scale strategy is formulated to obtain reproducible and deterministic phase-matching over the full wafer. Given its potential in large-scale quantum systems, the SPDC process is characterized in the proposed platform, establishing printable entangled photon-pair sources.

This work marks an important step toward scalable quadratic nonlinearities, paving the way for their integration into large-scale optical nonlinear systems and facilitating high-volume manufacturing of highly-efficient frequency converters.

#### 5. Experimental Section

**Fabrication of the Nonlinear Waveguide:** The fabrication starts from an X-cut LNOI wafer (300 nm LN/ 2 μm SiO<sub>2</sub> / LN substrate, NanoLN). The first step is periodic poling of the LN, for which electrodes (50 nm Cr/ 100 nm Au) are deposited through a sequence of electron-beam lithography, electron-beam evaporation and a liftoff process. The electrodes are covered with an insulating oil and the LN is poled through a single custom electric waveform, reaching a maximum voltage of 450 V. The quality of the inverted ferro-electric domains is characterized through PFM. After poling, the electrodes are removed through selective wet chemical etching.

Next, an amorphous silicon hard mask is deposited, which is patterned into rectangular films (40 μm × 1 mm) through a combination of UV-photolithography and reactive ion etching (RIE). The LN layer is then fully etched using an Ar-based RIE process, exposing the buried oxide layer underneath. The hard mask is subsequently selectively removed with a KOH solution. The PPLN films are reinforced through a patterned photoresist encapsulation, after which a long HF-based etch removes the oxide layer underneath the full PPLN film. This leaves the devices suspended with only a few LN attachment points to the side that keep them connected to the substrate.

In parallel, a silicon photonics chip is prepared. For the in-house platform, a uniform SiN chip (300 nm SiN/3.3 μm SiO<sub>2</sub>/Si substrate) is patterned through electron-beam lithography and RIE. For the foundry platform, a 200 mm silicon photonics wafer is provided by Imec, containing

300 nm thick SiN waveguides that are planarized through SiO<sub>2</sub> cladding followed by chemical-mechanical polishing. Before transfer printing, the chips are covered with photosensitive benzocyclobutene (BCB) where UV-lithography is used to remove the BCB directly above the nitride waveguides. This allows for adhesive bonding of the PPLN film on the sides, while having direct bonding on top of the waveguide. As a result, BCB thickness variations will not influence the phase-matching as the optical mode does not feel the BCB.

The PPLN devices are picked and printed onto the prepared silicon photonics chips with a polydimethylsiloxane (PDMS) stamp using a commercial micro-transfer printing tool (X-Celeprint μTP-100). The photoresist encapsulation is removed with acetone and a brief O<sub>2</sub> plasma. Lastly, the BCB is cured at 280°C for one hour in vacuum, leaving the devices ready for measurement.

**Precise Waveguide Modification:** To precisely tune the phase-matching, sub-nanometer control of the waveguide modifications is desired. This is achievable through strongly diluted wet chemical etching. In case of LN thinning, the modified RCA-1 clean (NH<sub>4</sub>OH:H<sub>2</sub>O<sub>2</sub> 1:4) etches 3 nm in 30 min at room temperature. For cladding control, a combination of SiO<sub>2</sub> deposition with a strongly diluted HF-based solution is similarly able to provide a sub-nanometer precision.

**Phase-Matching Characterization:** To characterize the phase-matching, the silicon photonics chips are cleaved to define facets of the silicon nitride waveguides. Coupling to the waveguides is achieved through two types of lensed fibers, which are single-mode at 1550 and 780 nm, respectively. The silicon photonics chips are designed symmetrically such that the fiber-to-PPLN losses can be characterized at both fundamental and second harmonic wavelength. Knowing the coupling losses at both wavelengths, the pump and generated second harmonic power are measured with calibrated detectors, allowing the corresponding powers in the hybrid waveguide to be calculated. The normalized nonlinear efficiency then results from:

$$\eta = \frac{P_{SH}}{P_F^2 \cdot L^2} \quad (1)$$

where  $P_F$  and  $P_{SH}$  are the fundamental and second harmonic power in the PPLN/SiN waveguide, respectively, and  $L$  is the nonlinear interaction length. Although the PPLN film has a total length of 1 mm, the interaction length is limited to 0.9 mm as 50 μm at each end remains unpoled where SiN waveguide transitions are located.

#### Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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#### Conflict of Interest

The authors declare no conflict of interest.

#### Author Contributions

T.V., S.C., and B.K. conceived the idea for the project. T.V., J.D.W., and L.D.J. have developed the processes (poling and transfer printing), and

fabricated the nonlinear waveguides. T.V., E.V., and L.D.J. designed and numerically simulated the components. T.V., L.D.J., and E.V. optically characterized the waveguides. T.V. and J.D.W. measured and characterized the SPDC process. S.J., P.V., N.S., D.B., M.D., F.F., P.A., S.B., and J.V.C. have developed Imec's 200 mm silicon photonics platform and provided the wafer. T.V. prepared figures, data and wrote the manuscript with input from co-authors. D.V.T., G.R., S.C., and B.K. supervised the project.

## Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

## Keywords

frequency conversion, integrated photonics, lithium niobate, photon-pair source

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